- Complete PWM Power Control Function
- Totem-Pole Outputs for 200-mA Sink or Source Current
- Output Control Selects Parallel or Push-Pull Operation
- Internal Circuitry Prohibits Double Pulse at Either Output
- Variable Dead-Time Provides Control Over Total Range
- Internal Regulator Provides a Stable 5-V Reference Supply, Trimmed to 1% Tolerance
- On-Board Output Current-Limiting Protection
- Undervoltage Lockout for Low V_{CC}
 Conditions
- Separate Power and Signal Grounds
- TL598Q Has Extended Temperature Range . . . -40°C to 125°C

D OR N PACKAGE (TOP VIEW) ERROR J 1IN+ 16 7 2IN+ ERROR AMP 2] 1IN−∏2 15 7 2IN-FEEDBACK **1** 3 14 REF DTC 4 13 OUTPUT CTRL 12 VCC СТ П RT [6 10 POWER GND SIGNAL GND 7 OUT1 [9 OUT2

description

The TL598 incorporates all the functions required in the construction of pulse-width-modulated (PWM) controlled systems on a single chip. Designed primarily for power-supply control, the TL598 provides the systems engineer with the flexibility to tailor the power-supply control circuits to a specific application.

The TL598 contains two error amplifiers, an internal oscillator (externally adjustable), a dead-time control (DTC) comparator, a pulse-steering flip-flop, a 5-V precision reference, undervoltage lockout control, and output control circuits. Two totem-pole outputs provide exceptional rise- and fall-time performance for power FET control. The outputs share a common source supply and common power ground terminals, which allow system designers to eliminate errors caused by high current-induced voltage drops and common-mode noise.

The error amplifier has a common-mode voltage range from 0 V to V_{CC} -2 V. The DTC comparator has a fixed offset that prevents overlap of the outputs during push-pull operation. A synchronous multiple supply operation can be achieved by connecting RT to the reference output and providing a sawtooth input to CT.

The TL598 device provides an output control function to select either push-pull or parallel operation. Circuit architecture prevents either output from being pulsed twice during push-pull operation. The output frequency

for push-pull applications is one-half the oscillator frequency $\left(f_{O} = \frac{1}{2 \text{ RT CT}}\right)$. For single-ended applications:

$$f_O = \frac{1}{RT CT}$$

The TL598C is characterized for operation from 0° C to 70° C. The TL598Q is characterized for operation from -40° C to 125° C.



Please be aware that an important notice concerning availability, standard warranty, and use in critical applications of Texas Instruments semiconductor products and disclaimers thereto appears at the end of this data sheet.



FUNCTION TABLE

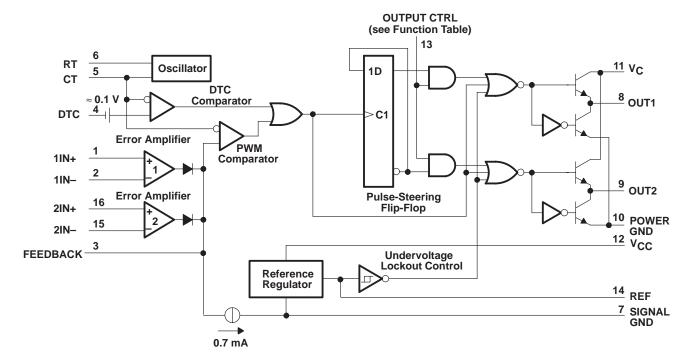
INPUT/OUTPUT CTRL	OUTPUT FUNCTION
V _I = GND	Single-ended or parallel output
$V_I = REF$	Normal push-pull operation

AVAILABLE OPTIONS

	PACKAGE		
TA	SMALL OUTLINE (D)	PLASTIC DIP (N)	CHIP FORM (Y)
0°C to 70°C	TL598CD	TL598CN	TL598Y
-40°C to 125°C	TL598QD	-	123901

Chip forms are tested at 25°C.

functional block diagram



TL598 PULSE-WIDTH-MODULATION CONTROL CIRCUITS

SLVS053C - FEBRUARY 1988 - REVISED JULY 1999

absolute maximum ratings over operating free-air temperature range (unless otherwise noted)†

Supply voltage, V _{CC} (see Note 1)	41 V
Amplifier input voltage, V _I	V _{CC} + 0.3 V
Collector voltage	41 V
Output current (each output), sink or source, IO	250 mA
Package thermal impedance, θ_{JA} (see Notes 2 and 3): D package	
N package	88°C/W
Lead temperature 1,6 mm (1/16 inch) from case for 10 seconds	260°C
Storage temperature range, T _{sto}	

[†] Stresses beyond those listed under "absolute maximum ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under "recommended operating conditions" is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

- NOTES: 1. All voltage values, except differential voltages, are with respect to the signal ground terminal.
 - 2. Maximum power dissipation is a function of $T_J(max)$, θ_{JA} , and T_A . The maximum allowable power dissipation at any allowable ambient temperature is $P_D = (T_J(max) T_A)/\theta_{JA}$. Operating at the absolute maximum T_J of 150°C can impact reliability.
 - 3. The package thermal impedance is calculated in accordance with JESD 51, except for through-hole packages, which use a trace length of zero.

recommended operating conditions

		MIN	MAX	UNIT
Supply voltage, V _{CC}		7	40	V
Amplifier input voltage, V _I		0	V _{CC} -2	V
Collector voltage		T	40	V
Output current (each output), sink or source, IO			200	mA
Current into feedback terminal, I _{IL}			0.3	mA
Timing capacitor, C _T		0.00047	10	μF
Timing resistor, R _T		1.8	500	kΩ
Oscillator frequency, f _{osc}		1	300	kHz
Operating free-air temperature, T _A	TL598C	0	70	°C
	TL598Q	-40	125	

electrical characteristics over recommended operating free-air temperature range, $V_{CC} = 15 \text{ V}$ (unless otherwise noted)

reference section (see Note 4)

PARAMETER TEST CONDITIONS		nizionio†		TL598C			UNIT		
PARAMETER	TEST CON	TEST CONDITIONS [†]		TYP [‡]	MAX	MIN	TYP [‡]	MAX	UNIT
Output voltage (REF)	lo - 1 mΛ	T _A = 25°C	4.95	5	5.05	4.95	5	5.05	V
Output voltage (NEF)	ge (REF) I _O = 1 mA	T _A = full range	4.9		5.1	4.9		5.1	V
Input regulation	$V_{CC} = 7 \text{ V to } 40 \text{ V}$	T _A = 25°C		2	25		2	22	mV
Output regulation	I _O = 1 mA to 10 mA	T _A = 25°C		1	15		1	15	mV
Output regulation	IQ = 1 IIIA to 10 IIIA	T _A = full range			50			80	IIIV
Output voltage change with temperature	$\Delta T_A = MIN \text{ to MAX}$			2	10		2	10	mV/V
Short-circuit output current§	REF = 0 V		-10	-48		-10	-48		mA

[†] Full range is 0°C to 70°C for the TL598C, and -40°C to 125°C for the TL598Q.

NOTE 4: Pulse-testing techniques that maintain the junction temperature as close to the ambient temperature as possible must be used.

oscillator section, $C_T = 0.001 \mu F$, $R_T = 12 k\Omega$ (see Figure 1) (see Note 4)

PARAMETER	TEST SOURIESUST	TL59	UNIT		
PARAMETER	TEST CONDITIONS [†]	MIN	TYP [‡]	MAX	UNII
Frequency			100		kHz
Standard deviation of frequency¶	All values of V _{CC} , C _T , R _T , T _A constant		100		Hz/kHz
Frequency change with voltage	$V_{CC} = 7 \text{ V to } 40 \text{ V}, \qquad T_A = 25^{\circ}\text{C}$		1	10	Hz/kHz
Frequency change with temperature#	$\Delta T_A = \text{full range}$		70	120	Hz/kHz
Frequency change with temperature#	$\Delta T_A = \text{full range}, \qquad C_T = 0.01 \ \mu\text{F}$		50	80	⊓Z/K∏Z

[†]Full range is 0°C to 70°C for the TL598C, and -40°C to 125°C for the TL598Q.

$$\sigma = \sqrt{\frac{\sum_{n=1}^{N} (x_n - \overline{x})^2}{N-1}}$$

NOTE 4. Pulse-testing techniques that maintain the junction temperature as close to the ambient temperature as possible must be used.

error amplifier section (see Note 4)

DADAMETER	PARAMETER TEST CONDITIONS			TL598			
PARAMETER				MIN	TYP‡	MAX	UNIT
Input offset voltage	FEEDBACK = 2.5 V				2	10	mV
Input offset current	FEEDBACK = 2.5 V				25	250	nA
Input bias current	FEEDBACK = 2.5 V				0.2	1	μΑ
Common-mode input voltage range	V _{CC} = 7 V to 40 V			0 to V _{CC} -2			V
Open-loop voltage amplification	ΔV_O (FEEDBACK) = 3 V	, V _O (FEEDBACK	() = 0.5 V to 3.5 V	70	95		dB
Unity-gain bandwidth					800		kHz
Common-mode rejection ratio	$V_{CC} = 40 \text{ V},$	$\Delta V_{IC} = 6.5 V$,	T _A = 25°C	65	80		dB
Output sink current (FEEDBACK)	FEEDBACK = 0.5 V			0.3	0.7		mA
Output source current (FEEDBACK)	FEEDBACK = 3.5 V			-2			mA
Phase margin at unity gain	FEEDBACK = 0.5 V to 3	5 V,	$R_L = 2 k\Omega$		65°		
Supply-voltage rejection ratio	FEEDBACK = 2.5 V,	$\Delta V_{CC} = 33 \text{ V},$	$R_L = 2 k\Omega$		100	·	dB

[‡] All typical values except for parameter changes with temperature are at $T_A = 25$ °C.

NOTE 4. Pulse-testing techniques that maintain the junction temperature as close to the ambient temperature as possible must be used.



[‡] All typical values except for parameter changes with temperature are at $T_A = 25$ °C.

[§] Duration of the short circuit should not exceed one second.

[‡] All typical values except for parameter changes with temperature are at $T_A = 25$ °C.

[¶] Standard deviation is a measure of the statistical distribution about the mean as derived from the formula:

[#] Effects of temperature on external R_T and C_T are not taken into account.

electrical characteristics over recommended operating free-air temperature range, $V_{CC} = 15 \text{ V}$ (unless otherwise noted)

undervoltage lockout section (see Note 4)

PARAMETER	TEST SOMETIONS!	TL598C		TL59	UNIT	
	TEST CONDITIONS [†]	MIN	MAX	MIN	MAX	UNII
Threshold voltage	T _A = 25°C	4	6	4	6	V
	ΔT_A = full range	3.5	6.9	3	6.9	V
I best and in T	T _A = 25°C	100		100		mV
Hysteresis [‡]	T _A = full range	50		30		IIIV

[†] Full range is 0°C to 70°C for the TL598C, and –40°C to 125°C for the TL598Q.

output section (see Note 4)

DADAMETED	TEST CO.	TL598C, T	LINUT		
PARAMETER	TEST CO	MIN	MAX	UNIT	
High-level output voltage	V _{CC} = 15 V,	$I_{O} = -200 \text{ mA}$	12		V
	$V_{CC} = 15 \text{ V},$ $V_{C} = 15 \text{ V}$	$I_{O} = -20 \text{ mA}$	13		V
Low lovel output voltage	V _{CC} = 15 V,	I _O = 200 mA		2	V
Low-level output voltage	V _C C = 15 V, V _C = 15 V	I _O = 20 mA		0.4	V
Output control input ourront	$V_I = V_{ref}$ $V_I = 0.4 \text{ V}$	_		3.5	mA
Output-control input current	V _I = 0.4 V		100	μΑ	

NOTE 4. Pulse-testing techniques must be used that maintain the junction temperature as close to the ambient temperature as possible.

dead-time control section (see Figure 1) (see Note 4)

PARAMETER	TEST CONDITIONS -	TL598C			TL598Q			UNIT
PARAMETER		MIN	TYP§	MAX	MIN	TYP§	MAX	UNIT
Input bias current (DTC)	V _I = 0 to 5.25 V		-2	-10		-2	-25	μΑ
Maximum duty cycle, each output	DTC = 0 V	0.45			0.45			
Input threshold voltage (DTC)	Zero duty cycle		3	3.3		3	3.2	
	Maximum duty cycle	0			0			V

 $[\]S$ All typical values except for parameter changes with temperature are at $T_A = 25$ °C.

pwm comparator section (see Note 4)

PARAMETER	TEST COMPITIONS	TL59	LINUT		
PARAMETER	TEST CONDITIONS		TYP§	MAX	UNIT
Input threshold voltage (FEEDBACK)	DTC = 0 V		3.75	4.5	V
Input sink current (FEEDBACK)	V(FEEDBACK) = 0.5 V	0.3	0.7		mA

[§] All typical values except for parameter changes with temperature are at T_A = 25°C.

total device (see Figure 1) (see Note 4)

PARAMETER	TEST CONDITIONS			TL598C, TL598Q			
PARAMETER				TYP§	MAX	UNIT	
Standby supply current	$RT = V_{ref}$	V _{CC} = 15 V		15	21	m ^	
	All other inputs and outputs open	V _{CC} = 40 V		20	26	mA	
Average supply current	DTC = 2 V			15		mA	

[§] All typical values except for parameter changes with temperature are at T_A = 25°C.

NOTE 4. Pulse-testing techniques must be used that maintain the junction temperature as close to the ambient temperature as possible.



[‡] Hysteresis is the difference between the positive-going input threshold voltage and the negative-going input threshold voltage.

NOTE 4. Pulse-testing techniques must be used that maintain the junction temperature as close to the ambient temperature as possible.

NOTE 4. Pulse-testing techniques must be used that maintain the junction temperature as close to the ambient temperature as possible.

NOTE Pulse-testing techniques must be used that maintain the junction temperature as close to the ambient temperature as possible.

electrical characteristics over recommended operating free-air temperature range, $V_{CC} = 15 \text{ V}$ (unless otherwise noted)

switching characteristics, $T_A = 25^{\circ}C$ (see Note 4)

PARAMETER	TEST CONDITIONS			TL59	TL598C, TL598Q		
PARAMETER	TEST CONDITIONS		MIN	TYP	MAX	UNIT	
Output-voltage rise time	CL = 1500 pF,	VC = 15 V,	VCC = 15 V,		60	150	ns
Output-voltage fall time	See Figure 2				35	75	113

NOTE 4. Pulse-testing techniques must be used that maintain the junction temperature as close to the ambient temperature as possible.

electrical characteristics, V_{CC} = 15 V, T_A = 25°C

reference section (see Note 4)

DARAMETER	TEST COMPLICATE	1			
PARAMETER	TEST CONDITIONS	MIN	TYP [†]	MAX	UNIT
Output voltage (REF)	I _O = 1 mA		5		V
Input regulation	V _{CC} = 7 V to 40 V		2		mV
Output regulation	I _O = 1 mA to 10 mA	1		mV	
Output-voltage change with temperature			2		mV/V
Short-circuit output current‡	REF = 0 V		-48		mA

[†] All typical values except for parameter changes with temperature are at $T_A = 25$ °C.

oscillator section, C_T = 0.001 μ F, R_T = 12 $k\Omega$ (see Figure 1) (see Note 4)

PARAMETER	TEST CONDITIONS	TL598Y			UNIT
PARAIVIETER	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Frequency			100		kHz
Standard deviation of frequency§	All values of V _{CC} , C _T , R _T , T _A constant		100		Hz/kHz
Frequency change with voltage	V _{CC} = 7 V to 40 V,		1		Hz/kHz

[§] Standard deviation is a measure of the statistical distribution about the mean as derived from the formula:

$$\sigma = \sqrt{\frac{\sum_{n=1}^{N} (x_n - \overline{X})^2}{N-1}}$$

NOTE 4. Pulse-testing techniques that maintain the junction temperature as close to the ambient temperature as possible must be used.

error amplifier section (see Note 4)

PARAMETER	TEST CONDITIONS	TL5	UNIT	
PARAMETER	TEST CONDITIONS	MIN T	YP MAX	UNII
Input offset voltage	Feedback = 2.5 V		2	mV
Input offset current	Feedback = 2.5 V		25	nA
Input bias current	Feedback = 2.5 V		0.2	μА
Open-loop voltage amplification	ΔV_{O} (FEEDBACK) = 3 V, V_{O} (FEEDBACK) = 0.5 V to 3.5 V		95	dB
Unity-gain bandwidth		8	300	kHz
Common-mode rejection ratio	$V_{CC} = 40 \text{ V}, \qquad \Delta V_{IC} = 6.5 \text{ V},$		80	dB
Output sink current (FEEDBACK)	FEEDBACK = 0.5 V		0.7	mA
Phase margin at unity gain	FEEDBACK = 0.5 V to 3.5 V, $R_L = 2 \text{ k}\Omega$		65°	
Supply-voltage rejection ratio	FEEDBACK = 2.5 V, ΔV_{CC} = 33 V, R_L = 2 k Ω	·	100	dB

NOTE 4. Pulse-testing techniques that maintain the junction temperature as close to the ambient temperature as possible must be used.



[‡] Duration of the short circuit should not exceed one second.

NOTE 4. Pulse-testing techniques that maintain the junction temperature as close to the ambient temperature as possible must be used.

electrical characteristics, $V_{CC} = 15 \text{ V}$, $T_A = 25^{\circ}\text{C}$

dead-time control section (see Figure 1) (see Note 4)

PARAMETER	TEST CONDITIONS	TL598Y			UNIT
PARAMETER	TEST CONDITIONS	MIN	TYP	MAX	UNII
Input bias current (DTC)	V _I = 0 to 5.25 V		-2		μΑ
Input threshold voltage (DTC)	Zero duty cycle		3		V

NOTE 4. Pulse-testing techniques that maintain the junction temperature as close to the ambient temperature as possible must be used.

pwm comparator section (see Note 4)

PARAMETER	TEST CONDITIONS	TL598Y			UNIT
	TEST CONDITIONS	MIN	TYP	MAX	UNII
Input threshold voltage (FEEDBACK)	DTC = 0 V		3.75		V
Input sink current (FEEDBACK)	FEEDBACK = 0.5 V		0.7		mA

NOTE 4. Pulse-testing techniques that maintain the junction temperature as close to the ambient temperature as possible must be used.

total device (see Figure 1) (see Note 4)

PARAMETER	TEST CONDITIONS	TL598Y			UNIT	
PARAMETER	TEST CONDITIONS		MIN	TYP	MAX	ONII
Standby supply current	$RT = V_{ref}$	$V_{CC} = 15 V$		15		mΛ
	All other inputs and outputs open	V _{CC} = 40 V		20		mA
Average supply current	DTC = 2 V			15		mA

NOTE 4. Pulse-testing techniques that maintain the junction temperature as close to the ambient temperature as possible must be used.

PARAMETER MEASUREMENT INFORMATION

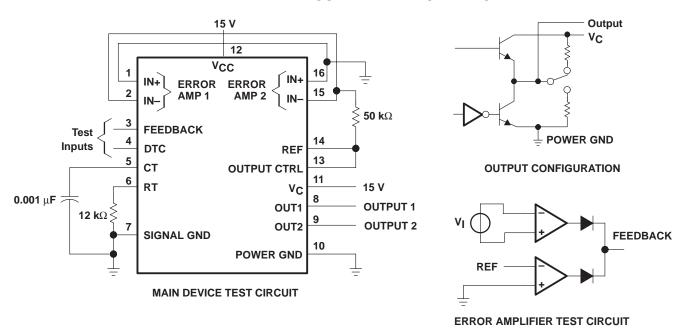


Figure 1. Test Circuits

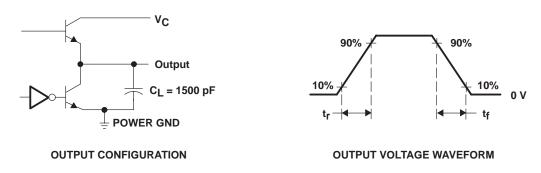


Figure 2. Switching Output Configuration and Voltage Waveform

TYPICAL CHARACTERISTICS

FREQUENCY VARIATION[†] **TIMING RESISTANCE** 100 k V_{CC} = 15 V 40 k f_{osc} - Oscillator Frequency - Hz **0.001** μ**F** 10 k $0.01 \mu F$ 4 k 1 k **0.1** μF 400 100 $C_T = 1 \mu F$ 40

OSCILLATOR FREQUENCY AND

40 k

 R_T – Timing Resistance – Ω

100 k

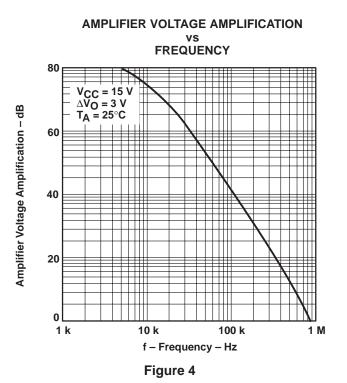
400 k

10 k

10

1 k

Figure 3



[†] Frequency variation (Δf) is the change in predicted oscillator frequency that occurs over the full temperature range.

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