SDAS216A - APRIL 1982 - REVISED DECEMBER 1994

- 3-State Versions of the 'ALS153 and SN74AS153
- Permits Multiplexing From n Lines to One Line
- Performs Parallel-to-Serial Conversion
- Package Options Include Plastic Small-Outline (D) Packages, Ceramic Chip Carriers (FK), and Standard Plastic (N) and Ceramic (J) 300-mil DIPs

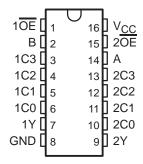
description

These data selectors/multiplexers contain inverters and drivers to supply full binary decoding data selection to the AND-OR gates. Separate output control inputs are provided for each of the two 4-line sections.

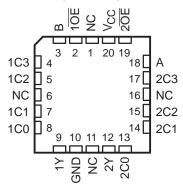
The 3-state outputs can interface with and drive data lines of bus-organized systems. With all but one of the common outputs disabled (at a high-impedance state), the low impedance of the single enabled output drives the bus line to a high or low logic level. Each output has its own output-enable (\overline{OE}) input. The output is disabled when \overline{OE} is high.

The SN54ALS253 is characterized for operation over the full military temperature range of -55°C to 125°C. The SN74ALS253 and SN74AS253A are characterized for operation from 0°C to 70°C.

SN54ALS253...J PACKAGE SN74ALS253, SN74AS253A...D OR N PACKAGE (TOP VIEW)



SN54ALS253 . . . FK PACKAGE (TOP VIEW)



NC - No internal connection

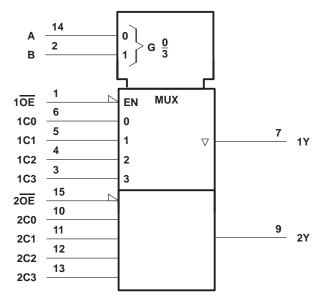
FUNCTION TABLE

	INPUTS							
SELI	ECT [†]	DATA			OE	OUTPUT Y		
В	Α	C0	C1	C2	C3	OE .		
Х	Х	Χ	X	Χ	X	Н	Z	
L	L	L	Χ	X	Χ	L	L	
L	L	Н	X	X	X	L	Н	
L	Н	Χ	L	X	X	L	L	
L	Н	Χ	Н	X	Χ	L	Н	
Н	L	Χ	Χ	L	Χ	L	L	
Н	L	Χ	Χ	Н	Χ	L	Н	
Н	Н	Х	Χ	Χ	L	L	L	
Н	Н	Х	Χ	Χ	Н	L	Н	

[†] Select inputs A and B are common to both sections.

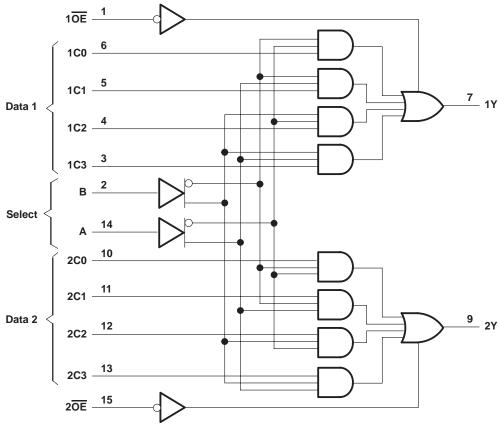
SDAS216A - APRIL 1982 - REVISED DECEMBER 1994

logic symbol[†]



[†] This symbol is in accordance with ANSI/IEEE Std 91-1984 and IEC Publication 617-12. Pin numbers shown are for the D, J, and N packages.

logic diagram (positive logic)



Pin numbers shown are for the D, J, and N packages.



SN54ALS253, SN74ALS253, SN74AS253A DUAL 1-OF-4 DATA SELECTORS/MULTIPLEXERS WITH 3-STATE OUTPUTS

SDAS216A - APRIL 1982 - REVISED DECEMBER 1994

absolute maximum ratings over operating free-air temperature range (unless otherwise noted)†

Supply voltage, V _{CC}	7 \
Input voltage, V _I	7 \
Voltage applied to a disabled 3-state output	5.5 \
Operating free-air temperature range, TA: SN54ALS253	55°C to 125°C
SN74ALS253	0°C to 70°C
Storage temperature range	−65°C to 150°C

recommended operating conditions

		SN	54ALS2	53	SN74ALS253		UNIT	
		MIN	NOM	MAX	MIN	NOM	MAX	UNIT
Vcc	Supply voltage	4.5	5	5.5	4.5	5	5.5	V
V _{IH}	High-level input voltage	2			2			V
V_{IL}	Low-level input voltage			0.7			0.8	V
IOH	High-level output current			-1			-2.6	mA
lOL	Low-level output current			12			24	mA
TA	Operating free-air temperature	-55		125	0		70	°C

electrical characteristics over recommended operating free-air temperature range (unless otherwise noted)

DADAMETED	TEST CONDITIONS		SN	54ALS2	53	SN74ALS253			
PARAMETER	1531 (1	TEST CONDITIONS		TYP [‡]	MAX	MIN	TYP [‡]	MAX	UNIT
VIK	V _{CC} = 4.5 V,	$I_{I} = -18 \text{ mA}$			-1.5			-1.5	V
	$V_{CC} = 4.5 \text{ V to } 5.5 \text{ V},$	$I_{OH} = -0.4 \text{ mA}$	V _{CC} -2	!		VCC -2	<u>)</u>		
VOH	Vaa = 4.5.V	$I_{OH} = -1 \text{ mA}$	2.4	3.3					V
	V _{CC} = 4.5 V	$I_{OH} = -2.6 \text{ mA}$				2.4	3.2		
\/o.	V _{CC} = 4.5 V	I _{OL} = 12 mA		0.25	0.4		0.25	0.4	V
VOL		I _{OL} = 24 mA					0.35	0.5	V
lozh	$V_{CC} = 5.5 V,$	$V_0 = 2.7 \text{ V}$			20			20	μΑ
lozL	$V_{CC} = 5.5 V,$	$V_0 = 0.4 \text{ V}$			-20			-20	μΑ
l _l	$V_{CC} = 5.5 V,$	V _I = 7 V			0.1			0.1	mA
lіН	$V_{CC} = 5.5 V,$	V _I = 2.7 V			20			20	μΑ
Ι _Ι Γ	$V_{CC} = 5.5 V,$	V _I = 0.4 V			-0.1			-0.1	mA
ΙΟ§	V _{CC} = 5.5 V,	V _O = 2.25 V	-20		-112	-30		-112	mA
la a	V 5.5.V	Outputs high		6.5	12		6.5	12	A
Icc	V _{CC} = 5.5 V	Outputs disabled		7.5	14		7.5	14	mA

[‡] All typical values are at $V_{CC} = 5 \text{ V}$, $T_A = 25^{\circ}\text{C}$.



[†] Stresses beyond those listed under "absolute maximum ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under "recommended operating conditions" is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

[§] The output conditions have been chosen to produce a current that closely approximates one half of the true short-circuit output current, IOS.

SN54ALS253, SN74ALS253, SN74AS253A DUAL 1-OF-4 DATA SELECTORS/MULTIPLEXERS WITH 3-STATE OUTPUTS

SDAS216A - APRIL 1982 - REVISED DECEMBER 1994

switching characteristics (see Figure 1)

PARAMETER	FROM (INPUT)	TO (OUTPUT)	C _L R1 R2 T _A	V_{CC} = 4.5 V to 5.5 V, C_L = 50 pF, R1 = 500 Ω, R2 = 500 Ω, T_A = MIN to MAX [†]			UNIT
			MIN	MAX	MIN	MAX	
t _{PLH}	A or B	Any Y	5	30	5	21	ns
t _{PHL}		Ally I	5	27	5	21	115
t _{PLH}	Data	Any Y	2	15	2	10	ns
^t PHL	(any C)	Ally f	3	18	3	14	115
^t PZH	ŌĒ	Any V	3	20	3	14	no
tPZL		Any Y	2	19	4	16	ns
^t PHZ	ŌĒ	Any Y	2	12	2	10	ns
^t PLZ	OE .	Ally I	2	18	2	14	115

[†] For conditions shown as MIN or MAX, use the appropriate value specified under recommended operating conditions.

absolute maximum ratings over operating free-air temperature range (unless otherwise noted)‡

Supply voltage, V _{CC}	7 V
Input voltage, V _I	7 V
Operating free-air temperature range, T _A : SN74AS253A	0°C to 70°C
Storage temperature range	-65°C to 150°C

[‡] Stresses beyond those listed under "absolute maximum ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under "recommended operating conditions" is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

recommended operating conditions

		SN	SN74AS253A		
		MIN	NOM	MAX	UNIT
VCC	Supply voltage	4.5	5	5.5	V
VIH	High-level input voltage	2			V
V _{IL}	Low-level input voltage			0.8	V
ІОН	High-level output current			-15	mA
l _{OL}	Low-level output current			48	mA
TA	Operating free-air temperature	0		70	°C

SN54ALS253, SN74ALS253, SN74AS253A DUAL 1-OF-4 DATA SELECTORS/MULTIPLEXERS WITH 3-STATE OUTPUTS

SDAS216A - APRIL 1982 - REVISED DECEMBER 1994

electrical characteristics over recommended operating free-air temperature range (unless otherwise noted)

	PARAMETER	TEST COND	NITIONS	SN7	4AS25	3A	UNIT
	PARAMETER	TEST CONL	ITIONS	MIN	TYP [†]	MAX	UNII
٧ıĸ		V _{CC} = 4.5 V,	I _I = -18 mA			-1.2	V
1/		$V_{CC} = 4.5 \text{ V to } 5.5 \text{ V},$	I _{OH} = -2 mA	V _{CC} -2			V
VOH		V _{CC} = 4.5 V,	I _{OH} = -15 mA	2.4	3.2		V
VOL		V _{CC} = 4.5 V,	I _{OL} = 48 mA		0.35	0.5	V
lozh		V _{CC} = 5.5 V,	V _O = 2.7 V			50	μА
lozL		V _{CC} = 5.5 V,	V _O = 0.4 V			-50	μΑ
	A, B	V FFV				0.2	A
11	All others	$V_{CC} = 5.5 \text{ V},$	V _I = 7 V			0.5 50 -50	mA
	A, B	V 55V	V 07V			40	^
lН	All others	V _{CC} = 5.5 V,	V _I = 2.7 V			20	μΑ
	A, B	V 55V	V 0.4V			-1	Δ
IΙL	All others	V _{CC} = 5.5 V,	V _I = 0.4 V			-0.5	mA
1 ₀ ‡	•	V _{CC} = 5.5 V,	V _O = 2.25 V	-30		-112	mA
			Outputs high		18	29	
ICC		V _{CC} = 5.5 V	Outputs low		20	32	mA
			Outputs disabled		21	33	

 $[\]dagger$ All typical values are at $V_{CC} = 5 \text{ V}$, $T_A = 25^{\circ}\text{C}$.

switching characteristics (see Figure 1)

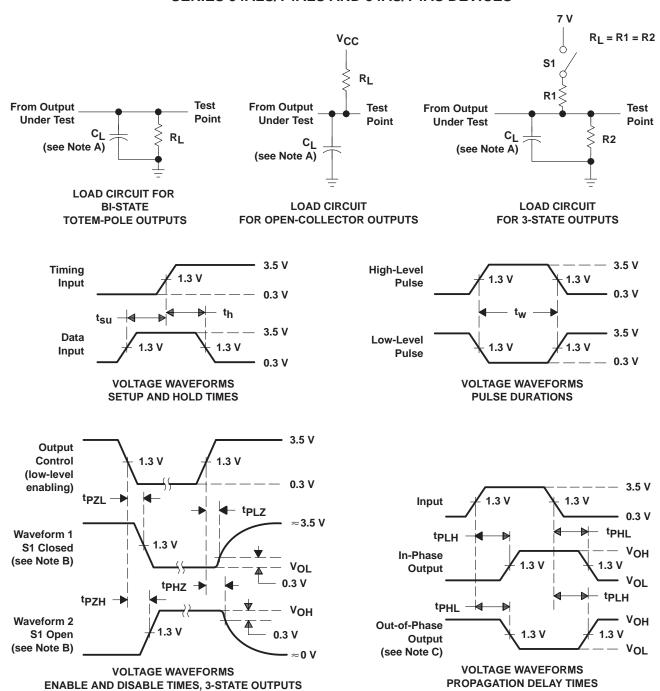
PARAMETER	FROM (INPUT)	TO (OUTPUT)	1 1 = - 000;		UNIT
			MIN	MAX	1
t _{PLH}	A or B	Y	3	13.5	ns
^t PHL	AOIB	1	3	11.5	
^t PLH	Data	Υ	2.5	7.5	
^t PHL	(any C)	1	2.5	8	ns
^t PZH	ŌĒ	Any Y	2	12.5	ns
t _{PZL}	OE .	Ally f	2.5	11.5	115
^t PHZ	ŌĒ	Any Y	1	6	200
t _{PLZ}	OE	Ally f	1	7	ns

[§] For conditions shown as MIN or MAX, use the appropriate value specified under recommended operating conditions.



[‡] The output conditions have been chosen to produce a current that closely approximates one half of the true short-circuit output current, los.

PARAMETER MEASUREMENT INFORMATION SERIES 54ALS/74ALS AND 54AS/74AS DEVICES



NOTES: A. C_L includes probe and jig capacitance.

- B. Waveform 1 is for an output with internal conditions such that the output is low except when disabled by the output control. Waveform 2 is for an output with internal conditions such that the output is high except when disabled by the output control.
- C. When measuring propagation delay items of 3-state outputs, switch S1 is open.
- D. All input pulses have the following characteristics: $PRR \le 1$ MHz, $t_f = t_f = 2$ ns, duty cycle = 50%.
- E. The outputs are measured one at a time with one transition per measurement.

Figure 1. Load Circuits and Voltage Waveforms



IMPORTANT NOTICE

Texas Instruments and its subsidiaries (TI) reserve the right to make changes to their products or to discontinue any product or service without notice, and advise customers to obtain the latest version of relevant information to verify, before placing orders, that information being relied on is current and complete. All products are sold subject to the terms and conditions of sale supplied at the time of order acknowledgement, including those pertaining to warranty, patent infringement, and limitation of liability.

TI warrants performance of its semiconductor products to the specifications applicable at the time of sale in accordance with TI's standard warranty. Testing and other quality control techniques are utilized to the extent TI deems necessary to support this warranty. Specific testing of all parameters of each device is not necessarily performed, except those mandated by government requirements.

CERTAIN APPLICATIONS USING SEMICONDUCTOR PRODUCTS MAY INVOLVE POTENTIAL RISKS OF DEATH, PERSONAL INJURY, OR SEVERE PROPERTY OR ENVIRONMENTAL DAMAGE ("CRITICAL APPLICATIONS"). TI SEMICONDUCTOR PRODUCTS ARE NOT DESIGNED, AUTHORIZED, OR WARRANTED TO BE SUITABLE FOR USE IN LIFE-SUPPORT DEVICES OR SYSTEMS OR OTHER CRITICAL APPLICATIONS. INCLUSION OF TI PRODUCTS IN SUCH APPLICATIONS IS UNDERSTOOD TO BE FULLY AT THE CUSTOMER'S RISK.

In order to minimize risks associated with the customer's applications, adequate design and operating safeguards must be provided by the customer to minimize inherent or procedural hazards.

TI assumes no liability for applications assistance or customer product design. TI does not warrant or represent that any license, either express or implied, is granted under any patent right, copyright, mask work right, or other intellectual property right of TI covering or relating to any combination, machine, or process in which such semiconductor products or services might be or are used. TI's publication of information regarding any third party's products or services does not constitute TI's approval, warranty or endorsement thereof.

Copyright © 1998, Texas Instruments Incorporated