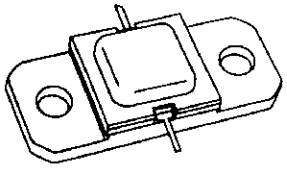
SGS-THOMSON MICROELECTRONICS

AM82731-006

RF & MICROWAVE TRANSISTORS S-BAND RADAR APPLICATIONS

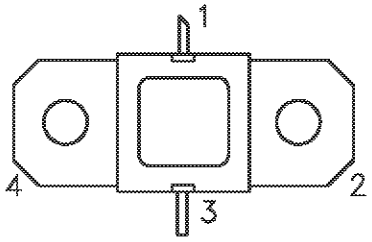
- REFRACTORY/GOLD METALLIZATION
- EMITTER SITE BALLASTED
- 5:1 VSWR CAPABILITY
- LOW THERMAL RESISTANCE
- INPUT/OUTPUT IMPEDANCE MATCHING
- OVERLAY GEOMETRY
- METAL/CERAMIC HERMETIC PACKAGE
- P_{OUT} = 5.5 W. MIN. WITH 5.6 dB GAIN
- BANDWIDTH = 400 MHz



.400 x .400 2NLFL (S042)
hermetically sealed

ORDER CODE	BRANDING
AM 82731-006	82731-6

PIN CONNECTION



1. Collector
2. Base
3. Emitter
4. Base

DESCRIPTION

The AM82731-006 device is a medium power silicon bipolar NPN transistor specifically designed for S-Band radar pulsed driver applications.

This device is capable of operation over a wide range of pulse widths, duty cycles, and temperatures and can withstand a 5:1 output VSWR. Low RF thermal resistance, refractory/gold metallization, and automatic wire bonding techniques ensure high reliability and product consistency.

The AM82731-006 is supplied in the hermetic metal/ceramic package with internal input/output impedance matching circuitry, and is intended for military and other high reliability applications.

ABSOLUTE MAXIMUM RATINGS (T_{case} = 25°C)

Symbol	Parameter	Value	Unit
P_{DISS}	Power Dissipation* ($T_C \leq 100°C$)	40	W
Ic	Device Current*	1.8	A
V_{CC}	Collector-Supply Voltage*	34	V
T_J	Junction Temperature (Pulsed RF Operation)	250	°C
T_{STG}	Storage Temperature	– 65 to +200	°C

THERMAL DATA

$R_{TH}(j\text{-}c)$	Junction-Case Thermal Resistance	3.75	°C/W

*Applies only to rated RF amplifier operation

ELECTRICAL SPECIFICATIONS (T_{case} = 25°C)

STATIC

Symbol	Test Conditions		Value			Unit
			Min.	Typ.	Max.	
BV_{CBO}	I_C = 5mA	I_E = 0mA	50	—	—	V
BV_{EBO}	I_E = 1mA	I_C = 0mA	3.5	—	—	V
BV_{CER}	I_C = 5mA	R_{BE} = 10Ω	50	—	—	V
I_{CES}	V_{CE} = 30V		—	—	4	mA
h_{FE}	V_{CE} = 5V	I_C = 500mA	10	—	—	—

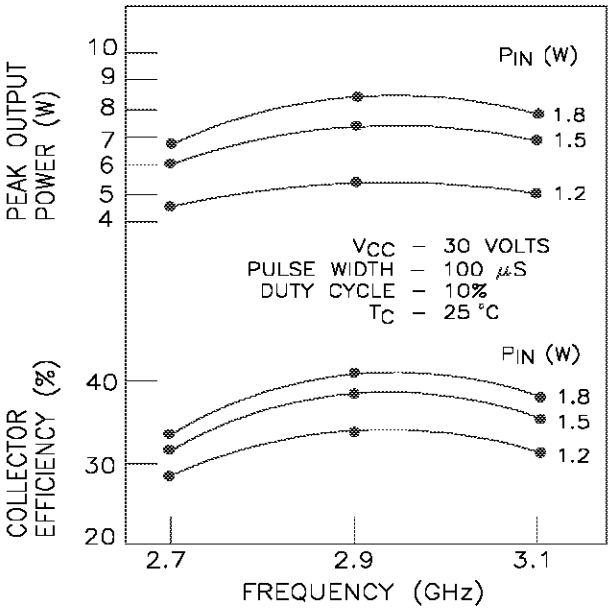
DYNAMIC

Symbol	Test Conditions			Value			Unit
				Min.	Typ.	Max.	
P_{OUT}	f = 2.7 — 3.1GHz	P_{IN} = 1.5W	V_{CC} = 30V	5.5	6.0	—	W
η_C	f = 2.7 — 3.1GHz	P_{IN} = 1.5W	V_{CC} = 30V	27	32	—	%
G_{PB}	f = 2.7 — 3.1GHz	P_{IN} = 1.5W	V_{CC} = 30V	5.6	6.0	—	dB

Note: Pulse Width = 100µS
Duty Cycle = 10%

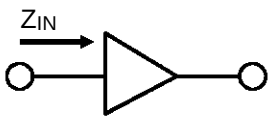
TYPICAL PERFORMANCE

TYPICAL BROADBAND PERFORMANCE

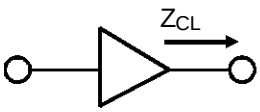


IMPEDANCE DATA

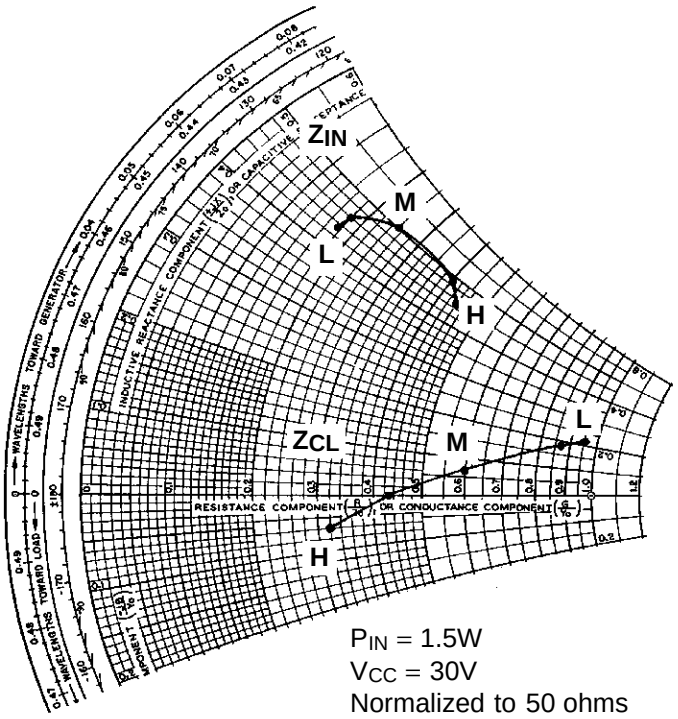
TYPICAL INPUT IMPEDANCES



TYPICAL COLLECTOR LOAD IMPEDANCES

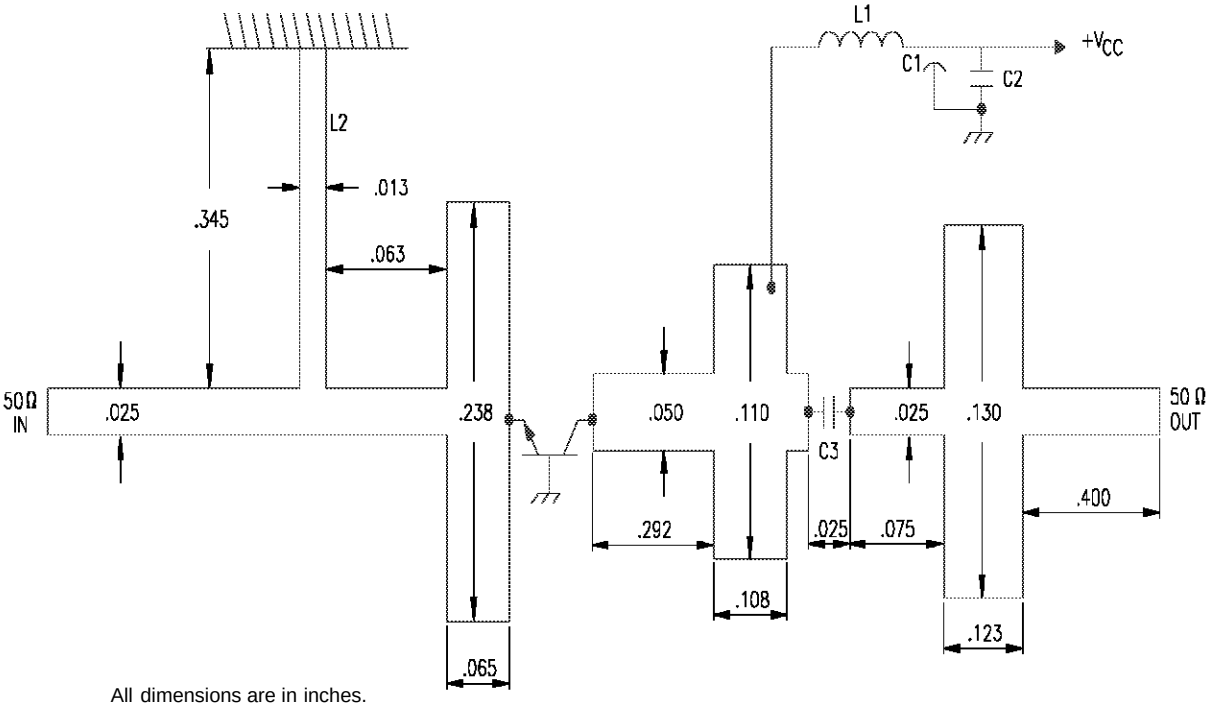


FREQ.	$Z_{IN}(\Omega)$	$Z_{CL}(\Omega)$
L = 2.7 GHz	9.0 + j 22.0	48.0 + j 11.5
• = 2.9 GHz	9.0 + j 23.0	43.0 + j 9.0
M = 3.1 GHz	12.5 + j 25.0	30.0 + j 3.0
• = 3.3 GHz	20.0 + j 25.0	21.5 + j 0.0
H = 3.5 GHz	22.0 + j 22.5	16.0 – j 3.0



P_{IN} = 1.5W
V_{CC} = 30V
Normalized to 50 ohms

TEST CIRCUIT

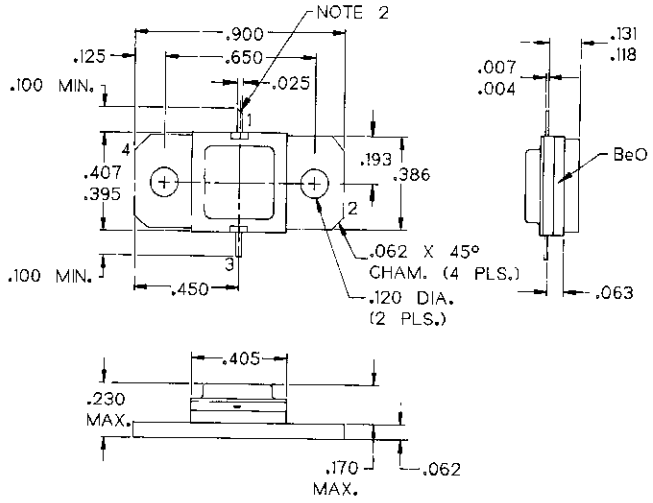


All dimensions are in inches.
Substrate material: .025 thick Al_2O_3 (Er = 9.6)

C1 : 1500 pF RF Feedthru
C2 : 100 µF Electrolytic
C3 : 100 pF Chip Capacitor
L1 : No. 32 Wire, 0.062 Inch Long
L2 : Printed RF Choke

PACKAGE MECHANICAL DATA

Ref.: Dwg. No.: J113214F



NOTES:
1. ALL TOLERANCE ± .010 EXCEPT WHERE NOTED; DIMENSIONS IN INCHES.
2. COLLECTOR LEAD SLANT CUT.