National Semiconductor Corporation

DM7875A/DM8875A, DM7875B/DM8875B TRI-STATE® 4-Bit Parallel Binary Multipliers

P4

a

General Description

Connection Diagram

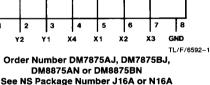
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These circuits are capable of multiplying together two 4-bit binary numbers when used together in pairs. The DM7875A/8875A provides the most significant four bits, and the DM7875B/8875B provides the least significant four bits. Since the largest number that can be obtained by multiplving two 4-bit numbers is 225 (15 \times 15), the eight output pins (four from each package) are sufficient to produce this number. Both the multiplier and the multiplicand must be connected to the eight input pins of each device. These devices are pin compatible with the SN54284/74284, and SN54285/74285; but have the advantage that these circuits provide either standard totem-pole TTL or TRI-STATE outputs. A gated two-input strobe control is provided. When either one, or both, of the strobe inputs is raised to a high logic level the outputs are forced into the high-impedance state. Thus, multiple devices may be connected to a common bus line.

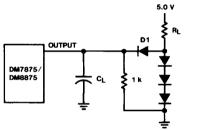
Features

- Pin compatible replacements for SN54284/74284 (DM7875A/8875A) SN54285/74285 (DM7875B/8875B)
- TRI-STATE outputs
- Typical propagation delay 35 ns

Dual-In-Line Package STRORE OUTPUTS P2 P3 \$2 P1 ¥٨ **S1** ٧сс 16 14 13 12 11 10 15



AC Test Circuit



TL/F/6592-2

Absolute Maximum Ratings (Note)

Specifications for Military/Aerospace products are not contained in this datasheet. Refer to the associated reliability electrical test specifications document.

Supply Voltage	7V
Input Voltage	5.5V
Operating Free Air Temperature Range	
DM78	-55°C to +125°C
DM88	0°C to +70°C
Storage Temperature Range	-65°C to +150°C

Note: The "Absolute Maximum Ratings" are those values beyond which the safety of the device cannot be guaranteed. The device should not be operated at these limits. The parametric values defined in the "Electrical Characteristics" table are not guaranteed at the absolute maximum ratings. The "Recommended Operating Conditions" table will define the conditions for actual device operation.

Recommended Operating Conditions

Symbol	Parameter	DM7875A			DM8875A			Units
		Min	Nom	Max	Min	Nom	Max	Units
V _{CC}	Supply Voltage	4.5	5	5.5	4.75	5	5.25	v
ViH	High Level Input Voltage	2			2			v
VIL	Low Level Input Voltage			0.8			0.8	v
ЮН	High Level Output Current			-2			-5.2	mA
IOL	Low Level Output Current			16			16	mA
TA	Free Air Operating Temperature	-55		125	0		70	°C

DM78/8875A Electrical Characteristics

over recommended operating free air temperature range (unless otherwise noted)

Symbol	Parameter	Cone	litions	Min	Typ (Note 1)	Max	Units
VI	Input Clamp Voltage	V _{CC} = Min, I _I =	= -12 mA			-1.5	v
V _{OH}	High Level Output Voltage	$V_{CC} = Min, I_{OH} = Max$ $V_{IL} = Max, V_{IH} = Min$		2.4			v
V _{OL}	Low Level Output Voltage		$V_{CC} = Min, I_{OL} = Max$ $V_{IH} = Min, V_{IL} = Max$			0.4	v
lj -	Input Current @ Max Input Voltage	$V_{CC} = Max, V_I = 5.5V$				1	mA
IIH	High Level Input Current	$V_{CC} = Max, V_1 = 2.4V$				40	μΑ
۱ _{IL}	Low Level Input Current	$V_{CC} = Max, V_I = 0.4V$				-1	mA
ЮZH	Off-State Output Current with High Level Output Voltage Applied	$V_{CC} = Max, V_O = 2.4V$ $V_{IH} = Min, V_{IL} = Max$				40	μΑ
lozl	Off-State Output Current with Low Level Output Voltage Applied	$V_{CC} = Max, V_O = 0.4V$ $V_{IH} = Min, V_{IL} = Max$				-40	μΑ
los	Short Circuit	V _{CC} = Max DM78		-20		-70	mA
	Output Current (Note 2)	DM88	-20		-70	mA	
lcc	Supply Current	V _{CC} = Max			75	110	mA

Note 1: All typicals are at V_{CC} = 5V, T_A = 25°C.

Note 2: Not more than one output should be shorted at a time.

Note 3: I_{CC} is measured with all inputs grounded.

DM78/8875A Switching Characteristics at $V_{CC} = 5V$ and $T_A = 25^{\circ}C$ (See Section 1 for Test Waveforms and Output Load)

		$R_{L} = 400\Omega$				
Symbol Parameter	Parameter	C _L =	5 pF	C _L =	50 pF	Units
	Min	Max	Min	Max		
^t РLН	Propagation Delay Time Low to High Level Output				60	ns
tPHL	Propagation Delay Time High to Low Level Output				60	ns
^t PZH	Output Enable Time to High Level Output				30	ns
tpzL	Output Enable Time to Low Level Output				30	ns
tрнz	Output Disable Time from High Level Output		30			ns
t _{PLZ}	Output Disable Time from Low Level Output		30			ns

Recommended Operating Conditions

Symbol	Parameter	DM7875B			DM8875B			Units
		Min	Nom	Max	Min	Nom	Max	
V _{CC}	Supply Voltage	4.5	5	5.5	4.75	5	5.25	v
VIH	High Level Input Voltage	2			2			v
VIL	Low Level Input Voltage			0.8			0.8	v
ЮН	High Level Output Current			-2			-5.2	mA
IOL	Low Level Output Current			16			16	mA
TA	Free Air Operating Temperature	- 55		125	0		70	°C

Symbol	Parameter	Con	ditions	Min	Typ (Note 1)	Max	Units
VI	Input Clamp Voltage	$V_{CC} = Min, I_1$	= -12 mA			- 1.5	V
V _{OH}	High Level Output Voltage	$V_{CC} = Min, I_O$ $V_{IL} = Max, V_{II}$		2.4			v
V _{OL}	Low Level Output Voltage	$V_{CC} = Min, I_O$ $V_{IH} = Min, V_{IL}$	-			0.4	v
łı	Input Current @ Max Input Voltage	V _{CC} = Max, V	I = 5.5V			1	mA
Ιн	High Level Input Current	V _{CC} = Max, V	= 2.4V			40	μA
կլ	Low Level Input Current	V _{CC} = Max, V	I = 0.4∨			-1	mA
lozн	Off-State Output Current with High Level Output Voltage Applied	$V_{CC} = Max, V_{IL}$ $V_{IH} = Min, V_{IL}$	•			40	μΑ
OZL	Off-State Output Current with Low Level Output Voltage Applied	V _{CC} = Max, V _I V _{IH} = Min, V _{IL}	0			-40	μΑ
os	Short Circuit	V _{CC} = Max	DM78	-20		-70	4
	Output Current	(Note 2)	DM88	-20		-70	mA
lcc	Supply Current	V _{CC} = Max (N	ote 3)		75	110	mA

Note 1: All typicals are at $V_{CC} = 5V$, $T_A = 25^{\circ}C$.

Note 2: Not more than one output should be shorted at a time.

Note 3: I_{CC} is measured with all inputs grounded.

DM78/8875B Switching Characteristics at $V_{CC} = 5V$ and $T_A = 25^{\circ}C$ (See Section 1 for Test Waveforms and Output Load)

Symbol		$R_L = 400\Omega$				
	Parameter	C _L =	5 pF	C _L =	Units	
		Min	Max	Min	Max	1
t _{PLH}	Propagation Delay Time Low to High Level Output				60	ns
^t PHL	Propagation Delay Time High to Low Level Output				60	ns
tрzн	Output Enable Time to High Level Output				30	ns
t _{PZL}	Output Enable Time to Low Level Output				30	ns
t _{PHZ}	Output Disable Time from High Level Output		30			ns
t _{PLZ}	Output Disable Time from Low Level Output		30			ns

