

Absolute Maximum Ratings (Note)
If Military/Aerospace specified devices are required, please contact the National Semiconductor Sales Office/Distributors for availability and specifications.
Supply Voltage 8V
Input Voltage 5.5 V

Operating Free Air Temperature Range
DM54L
$-55^{\circ} \mathrm{C}$ to $+125^{\circ} \mathrm{C}$
Storage Temperature Range $\quad-65^{\circ} \mathrm{C}$ to $+150^{\circ} \mathrm{C}$

Note: The "Absolute Maximum Ratings" are those values beyond which the safety of the device cannot be guaranteed. The device should not be operated at these limits. The parametric values defined in the "Electrical Characteristics" table are not guaranteed at the absolute maximum ratings. The "Recommended Operating Conditions" table will define the conditions for actual device operation.

## Recommended Operating Conditions

| Symbol | Parameter |  | DM54L93 |  |  | Units |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  |  | Min | Nom | Max |  |
| $\mathrm{V}_{\mathrm{CC}}$ | Supply Voltage |  | 4.5 | 5 | 5.5 | V |
| $\mathrm{V}_{\text {IH }}$ | High Level Input Voltage |  | 2 |  |  | V |
| $\mathrm{V}_{\text {IL }}$ | Low Level Input Voltage |  |  |  | 0.7 | V |
| $\mathrm{IOH}^{\text {I }}$ | High Level Output Current |  |  |  | -0.2 | mA |
| lOL | Low Level Output Current |  |  |  | 2 | mA |
| $\mathrm{f}_{\text {CLK }}$ | Clock Frequency (Note 5) |  | 0 |  | 6 | MHz |
| tw | Pulse Width (Note 5) | A | 90 |  |  | ns |
|  |  | B | 90 |  |  |  |
|  |  | Reset | 200 |  |  |  |
| $t_{\text {REL }}$ | Reset Release time (Note 5) |  | 200 |  |  | ns |
| $\mathrm{T}_{\text {A }}$ | Free Air Operating Temperature |  | -55 |  | 125 | ${ }^{\circ} \mathrm{C}$ |

Electrical Characteristics over recommended operating free air temperature (unless otherwise noted)

| Symbol | Parameter | Conditions |  | Min | Typ (Note 1) | Max | Units |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| $\mathrm{V}_{\mathrm{OH}}$ | High Level Output Voltage | $\begin{aligned} & \mathrm{V}_{\mathrm{CC}}=\mathrm{Min}, \mathrm{I}_{\mathrm{OH}}=\operatorname{Max} \\ & \mathrm{V}_{\mathrm{IL}}=\mathrm{Max}, \mathrm{~V}_{\mathrm{IH}}=\operatorname{Min} \end{aligned}$ |  | 2.4 | 3.4 |  | V |
| $\mathrm{V}_{\mathrm{OL}}$ | Low Level Output Voltage | $\begin{aligned} & \mathrm{V}_{\mathrm{CC}}=\mathrm{Min}, \mathrm{I}_{\mathrm{OL}}=\mathrm{Max} \\ & \mathrm{~V}_{\mathrm{IL}}=\mathrm{Max}, \mathrm{~V}_{\mathrm{IH}}=\mathrm{Min} \\ & \text { (Note 4) } \end{aligned}$ |  |  | 0.15 | 0.3 | V |
| 1 | Input Current @ Max Input Voltage | $\begin{aligned} & \mathrm{V}_{\mathrm{CC}}=\mathrm{Max} \\ & \mathrm{~V}_{1}=5.5 \mathrm{~V} \end{aligned}$ | Reset |  |  | 0.1 | mA |
|  |  |  | A |  |  | 0.2 |  |
|  |  |  | B |  |  | 0.2 |  |
| $\mathrm{IIH}^{\text {H}}$ | High Level Input Current | $\begin{aligned} & V_{C C}=M a x \\ & V_{1}=2.4 V \end{aligned}$ | Reset |  |  | 10 | $\mu \mathrm{A}$ |
|  |  |  | A |  |  | 20 |  |
|  |  |  | B |  |  | 20 |  |
| IIL | Low Level Input Current | $\begin{aligned} & V_{C C}=\mathrm{Max} \\ & \mathrm{~V}_{\mathrm{I}}=0.3 \mathrm{~V} \end{aligned}$ | Reset |  |  | $-0.18$ | mA |
|  |  |  | A |  |  | -0.36 |  |
|  |  |  | B |  |  | -0.36 |  |
| los | Short Circuit Output Current | $\begin{aligned} & \mathrm{V}_{\mathrm{CC}}=\mathrm{Max} \\ & (\text { Note 2) } \end{aligned}$ |  | -3 |  | -15 | mA |
| $\mathrm{I}_{\mathrm{CC}}$ | Supply Current | $\mathrm{V}_{\text {CC }}=\mathrm{Max}$ (Note 3) |  |  |  | 5.5 | mA |

Note 1: All typicals are at $\mathrm{V}_{\mathrm{CC}}=5 \mathrm{~V}, \mathrm{~T}_{\mathrm{A}}=25^{\circ} \mathrm{C}$.
Note 2: Not more than one output should be shorted at a time.
Note 3: $I_{C C}$ is measured with all outputs open, RO inputs grounded following momentary connection to 4.5 V and all other inputs grounded
Note 4: $Q_{A}$ outputs are tested at $I_{O L}=m a x$ plus the limit value of $I_{I L}$ for the $B$ input. This permits driving the $B$ input while maintaining full fan-out capability
Note 5: $\mathrm{T}_{\mathrm{A}}=25^{\circ} \mathrm{C}$ and $\mathrm{V}_{\mathrm{CC}}=5 \mathrm{~V}$.

| Symbol | Parameter | From (Input) To (Output) | $\mathrm{R}_{\mathrm{L}}=4 \mathrm{k} \Omega, \mathrm{C}_{\mathrm{L}}=50 \mathrm{pF}$ |  | Units |
| :---: | :---: | :---: | :---: | :---: | :---: |
|  |  |  | Min | Max |  |
| $\mathrm{f}_{\text {MAX }}$ | Maximum Clock Frequency | A to $Q_{A}$ | 6 |  | MHz |
| $t_{\text {PLH }}$ | Propagation Delay Time Low to High Level Output | A to $Q_{D}$ |  | 400 | ns |
| $t_{\text {PHL }}$ | Propagation Delay Time High to Low Level Output | A to $Q_{D}$ |  | 400 | ns |

## Logic Diagram



The J and K inputs shown without connection are for reference only and are functionally at a high level

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