

BMR - 0101

BMR-0101 is the low reset type of IC that guarantee to set again micro computers or logic systems by detecting the intermittent of fluctuating power supply voltage during normal use or switching on/off of the equipments. A comparator type of hysteresis transistor developed by KODENSHI is built in the IC, so that BMR-0101 is very cost effective components.

FEATURES

- Low current consumption
- Low operation voltage
- High current of output transistor
- Hysteresis circuit built in

APPLICATIONS

- Micro computer circuits in mobile phones, word processors, TVs, VCRs etc.
- General logic circuits
- Detection of voltage drop in batteries of note personal computers, mobile phones
- Switching to backup power supply

MAXIMUM RATINGS

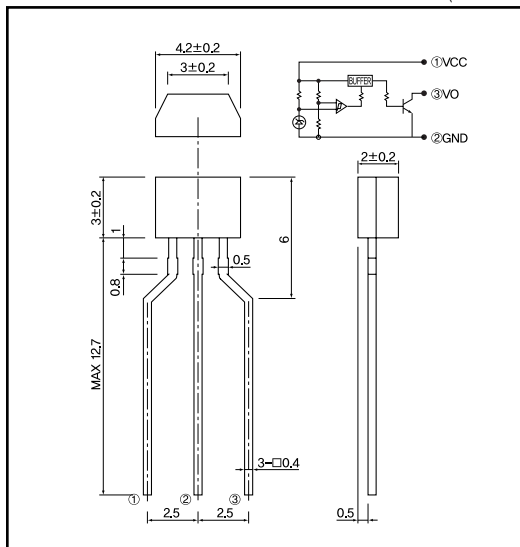
(Ta=25)

Item	Symbol	Rating	Unit
Supply voltage	V _{CC}	- 0.3 ~ + 10.0	V
Power dissipation	P _D	200	mW
Operating temp.	T _{opr.}	- 20 ~ + 75	
Storage temp.	T _{stg.}	- 40 ~ + 125	
Soldering temp.*1	T _{sol.}	260	

*1.5sec at location of 2mm away from lead bottom.

DIMENSIONS

(Unit : mm)



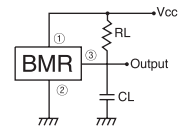
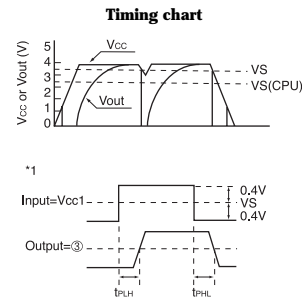
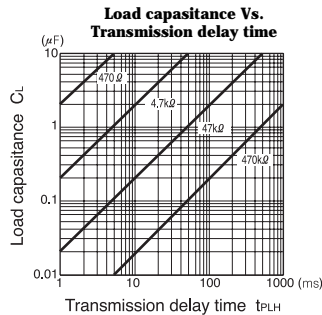
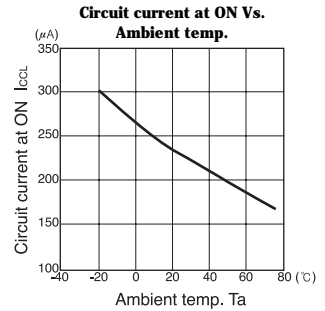
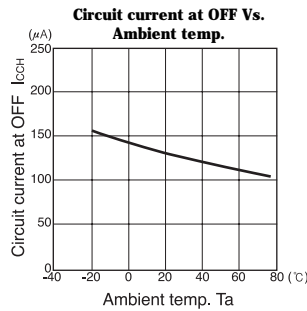
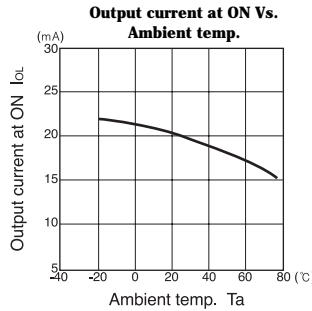
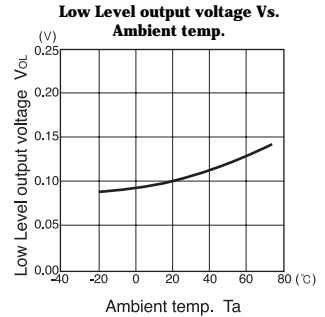
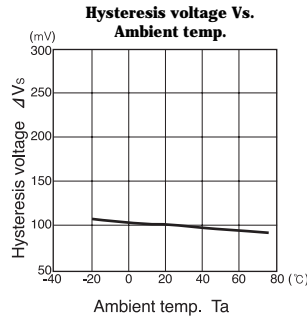
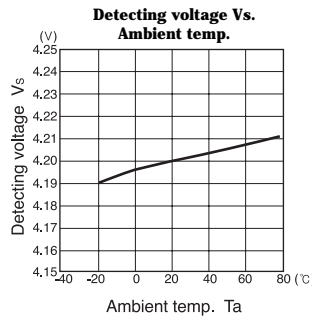
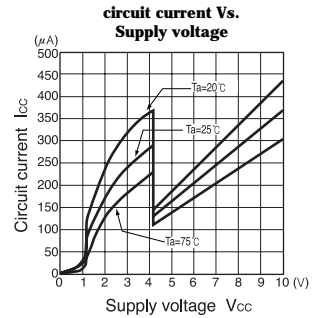
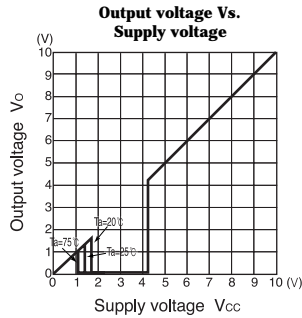
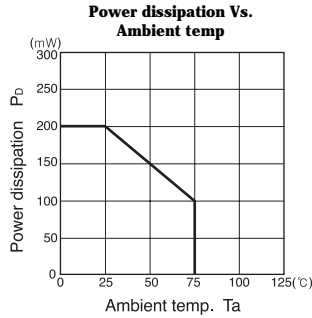
ELECTRO-OPTICAL CHARACTERISTICS

(V_c=5V, Ta=25)

Item	Symbol	Conditions	Min.	Typ.	Max.	Unit.
Detecting Voltage	V _S	R _L = 470 Ω, V _{CC} = H, I _L , V _{OL} = 0.4V	4.3	4.5	4.7	V
			4.0	4.2	4.4	
			3.7	3.9	4.1	
			3.4	3.6	3.8	
			3.1	3.3	3.5	
			2.9	3.1	3.3	
			2.75	2.9	3.05	
			2.55	2.7	2.85	
			2.35	2.5	2.65	
			2.15	2.3	2.45	
Hysteresis voltage	V _S	R _L = 470 Ω, V _{CC} = L, H, L	40	100	300	mV
Temperature coefficient of detecting voltage	V _S / T	R _L = 470 Ω, Ta = -20 ~ 75	-	± 0.01	-	%/°C
Low level output voltage	V _{OL}	R _L = 470 Ω, V _{CC} = V _S Min.	-	0.1	0.4	V
Circuit current at ON	I _{OCL}	R _L = 0, V _{CC} = V _S Min.	-	230	380	μA
Circuit current at OFF	I _{OCH}	R _L = 0, V _{CC} = 5.0V	-	130	200	μA
Threshold operating voltage	V _{OPL}	R _L = 4.7k Ω, V _{OL} = 0.4V	-	1.3	1.5	V
Output current at ON 1	I _{OL} 1	R _L = 0, V _{CC} = V _S Min.	10	20	-	mA
Output current at ON 2	I _{OL} 2	R _L = 0, Ta = -20 ~ 75	5	-	-	mA
Transmission delay time	t _{PLH}	R _L = 4.7k Ω, C _L = 100pF	-	12	35	μsec
Transmission delay time	t _{PHL}	R _L = 4.7k Ω, C _L = 100pF	-	2	7	μsec

System Reset IC

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V_{CC} : V
 $V_{S(CPU)}$: Reset threshold voltage of CPU/MPU
 CL : μF
 RL : $k\Omega$
 Caution) It is desirable that Capacitor be built between ① and ② terminal when high impedance of V_{CC} line, unstable power line or high ripple occurrence to expected.