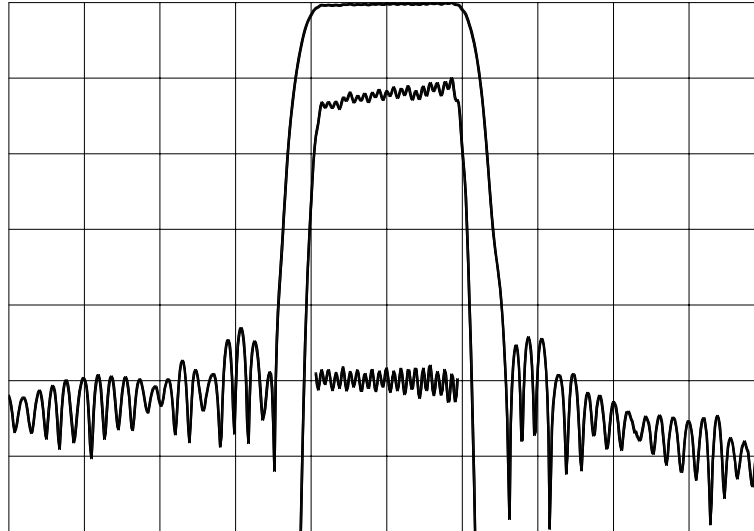




TYPICAL PERFORMANCE



Horizontal: 8 MHz/div

Vertical (from top): Magnitude 10 dB/div
Magnitude 1 dB/div
Group Delay 50 nsec/div

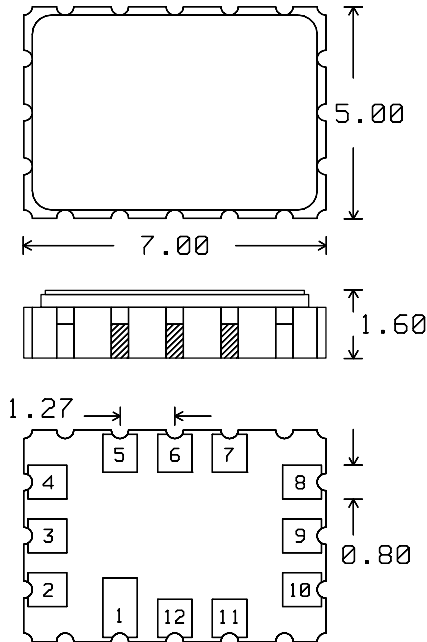
SPECIFICATION

Parameter	Min	Typ	Max	Units
Center Frequency (F_c) ¹		326.4		MHz
Insertion Loss		15	18	dB
2dB Bandwidth	15.0	16.8		MHz
40 dB Bandwidth		24.3	26	MHz
Rejection (10 MHz to 50 MHz)	40			dB
Rejection (50 MHz to F_c -50 MHz)	47	52		dB
Rejection (F_c -50 MHz to F_c -13 MHz)	40	45		dB
Rejection (F_c +13 MHz to F_c +50 MHz)	40	45		dB
Rejection (F_c +50 MHz to 450 MHz)	47	57		dB
Passband Amplitude Ripple ²		0.2	0.5	dB p-p
Passband Group Delay Ripple ³		30	50	ns p-p
Source and Load Impedance		50		Ω
Operating Temperature Range	-10	23	+80	$^{\circ}$ C

- Notes:
1. Fixed reference. All specified bandwidths are centered at this frequency.
 2. Over $F_c \pm 2.5$ MHz.
 3. Over $F_c \pm 7.5$ MHz.



PACKAGE OUTLINE

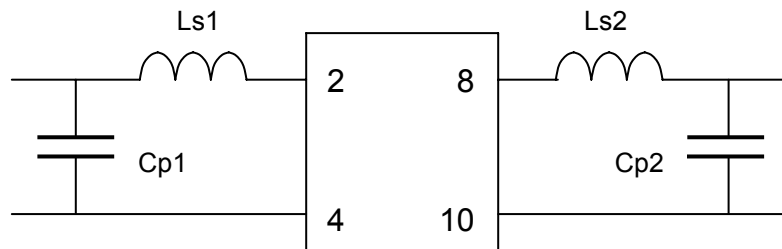


Units: mm

Pin Configuration:

Input: 2
 Output: 8
 Ground: 1,3,4,5,6,7,9,10,11,12

MATCHING CIRCUIT



Typical component values: Ls1 = 25 nH Cp1 = 20 pF Ls2 = 22 nH Cp2 = 20 pF
 (minimum inductor Q = 45)

- Notes
- Requires 2% matching components.
 - Component values may change depending on board layout.
 - All pins other than 2 and 8 are connected together internally to the package

