HD74AC02

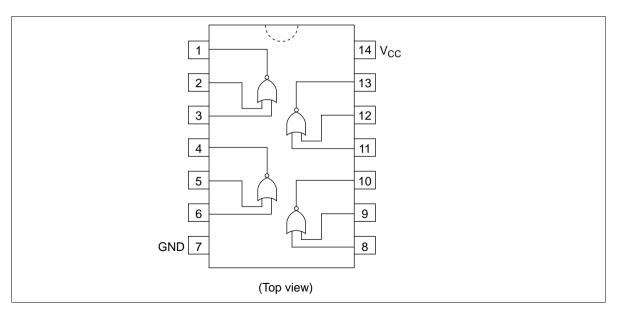
Quad 2-Input NOR Gate

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Feature

Outputs Source/Sink 24 mA

Pin Arrangement



DC Characteristics (unless otherwise specified)

Item	Symbol	Max	Unit	Condition
Maximum quiescent supply current	I _{cc}	40	μΑ	$V_{IN} = V_{CC}$ or ground, $V_{CC} = 5.5 \text{ V}$, Ta = Worst case
Maximum quiescent supply current	I _{cc}	4.0	μΑ	$V_{IN} = V_{CC}$ or ground, $V_{CC} = 5.5 \text{ V}$, Ta = 25°C



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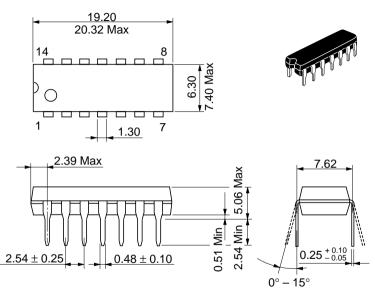
AC Characteristics

			Ta = +25°C C _L = 50 pF			Ta = -40° C to $+85^{\circ}$ C C _L = 50 pF		
Item	Symbol	V _{cc} (V)*1	Min	Тур	Max	Min	Max	Unit
Propagation delay	t _{PLH}	3.3	1.0	5.0	7.5	1.0	8.0	ns
		5.0	1.0	4.0	6.0	1.0	6.5	
Propagation delay	t _{PHL}	3.3	1.0	5.0	7.5	1.0	8.0	ns
		5.0	1.0	4.5	6.5	1.0	7.0	

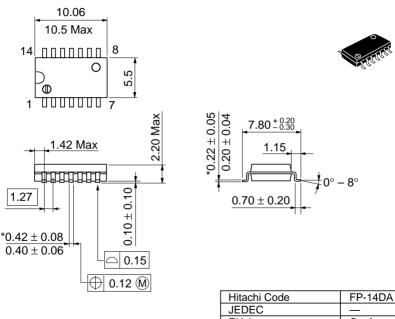
Note: 1. Voltage Range 3.3 is 3.3 V \pm 0.3 V Voltage Range 5.0 is 5.0 V \pm 0.5 V

Capacitance

Item	Symbol	Тур	Unit	Condition
Input capacitance	C _{IN}	4.5	pF	V _{CC} = 5.5 V
Power dissipation capacitance	C_{PD}	30.0	pF	$V_{CC} = 5.0 \text{ V}$



Hitachi Code	DP-14
JEDEC	Conforms
EIAJ	Conforms
Weight (reference value)	0.97 g

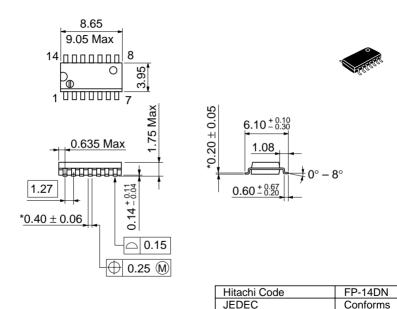


*Dimension including the plating thickness
Base material dimension

*Dimension including the plating thickness

Base material dimension

*United States of The 14-57 of The 14



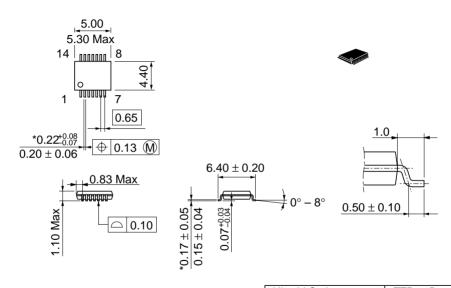
EIAJ

Weight (reference value)

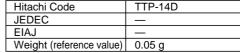
Conforms

0.13 g

*Pd plating



*Dimension including the plating thickness
Base material dimension



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