## HD14585B

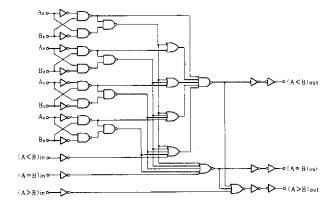
#### 4-bit Magnitude Comparator

The HD14585B 4-bit Magnitude Comparator has eight comparing inputs (A3, B3, A2, B2, A1, B1, A0, B0), three cascading inputs (A < B, A = B and A > B), and three outputs (A < B, A = B, A > B). This device compares two 4-bit words (A and B) and determines whether they are "less than", "equal to", or "greater than" by a high level on the appropriate output. For words greater than 4-bits, units can be cascaded by connecting outputs (A < B), and (A = B) to the corresponding inputs of the next significant comparator (input A > B is connected to a high). Inputs (A < B), (A = B), and (A > B) on the least significant (first) comparator are connected to a low, a high, and a high, respectively. Applications include logic in CPU's, correction and/or detection of instrumentation conditions, comparator in testers, converters, and controls.

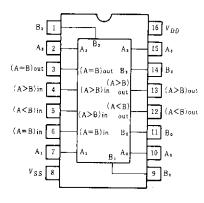
#### **■** FEATURES

- Quiescent Current = 5nA/pkg typ. @5V
- Expandable
- Applicable to Binary or 8421-BCD Code
- Supply Voltage Range = 3 to 18V
- Capable of Driving One Low-power Schottky TTL Load Over the Rated Temperature Range

#### **■LOGIC DIAGRAM**



### **■ PIN ARRANGEMENT**



(Top View)

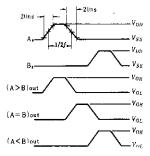
#### TRUTH TABLE

Inputs							0			
	С	ascadi	ing	Outputs						
A <sub>3</sub> , B <sub>3</sub>	A2, B2	$A_1, B_1$	A0, B0	A < B	A = B	A >B	A≤B	A = B	A>B	
A3>B1	×	×	×	×	×	1	0	0	1	
$A_3 = B_3$	$A_2 > B_2$	×	×	×	×	1	0	0	1	
$A_3 = B_3$	$A_2 = B_2$	A,>B,	×	×	×	1	0	0	1	
$A_3 = B_3$	$A_2 = B_2$	$A_1 = B_1$	$A_0 > B_0$	×	×	1	0	0	1	
$A_3 = B_3$	$A_2 = B_2$	A,=B,	$A_0 = B_0$	0	0	1	0	0	1	
A <sub>3</sub> =B <sub>3</sub>	$A_2 = B_2$	$A_1 = B_1$	$\mathbf{A}_0 = \mathbf{B}_0$	0	1	1	0	1	0	
$A_3 = B_3$	$A_2 = B_2$	$A_1 = B_1$	$A_0 = B_0$	1	0	ı	1	0	0	
A <sub>3</sub> =B <sub>3</sub>	$A_2 = B_2$	$A_1 = B_1$	A <sub>0</sub> <b<sub>0</b<sub>	×	×	×	1	0	0	
$A_3 = B_3$	$A_2 = B_2$	$A_i \le B_i$	×	×	×	×	1	0	0	
$A_3 = B_3$	$A_z \le B_z$	×	×	×	×	Х	1	0	0	
$A_3 \le B_3$	×	×	×	×	×	×	1	0	0	

<sup>× :</sup> Don't Care

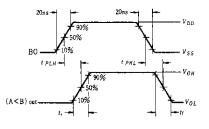
#### **■ DYNAMIC SIGNAL WAVEFORMS**

#### Power Dissipation Signal Waveform



Notes) 1. Inputs (A>B) and (A=B) high, and inputs  $B_2,A_2,B_1,A_1,B_0,A_0$  and (A<B) low.

#### ● Dynamic Signal Waveforms



Note) Inputs (A>B) and (A=B) high, and inputs  $B_2, A_3, B_2, A_3, B_1, A_1, A_0$ , and  $(A \le B)$  low.

 $<sup>{\</sup>bf 2}$  . f in respect to a system clock.

#### ■ ELECTRICAL CHARACTERISTICS

Characteristic	Symbol		Van(V) Test Conditions 40°C	25°C			85°C					
Onar acter 13the	Dymoor	$V_{DD}(V)$	Test Conditions	min	max	min	typ	max	min	max	Unit	
	Vol	5.0			0.05		0	0.05	-	0.05	v	
		10	$V_{in} = V_{DD}$ or $0$	_	0.05	_	0	0.05	-	0.05		
Output Voltage		15			0.05	_	0	0.05	_	0.05		
	Voн	5.0		4.95	-	4.95	5.0	_	4.95		v	
		10	$V_{in}\!=0$ or $V_{DD}$	9.95		9.95	10	_	9.95	-		
		15		14.95	_	14.95	15		14.95	_		
		5.0	Vout = 4.5 or 0.5V	-	1.5	_	2.25	1.5	-	1.5		
Input Voltage -	$V_{IL}$	10	$V_{out} = 9.0 \text{ or } 1.0\text{V}$	-	3.0	-	4.50	3.0	_	3.0	V	
		15	$V_{\text{out}} = 13.5 \text{ or } 1.5 \text{V}$ - 4.0 - 6.		6.75	4.0	-	4.0	V V V V W mA μA pF			
		5.0	$V_{out} = 0.5 \text{ or } 4.5\text{V}$	3.5	_	3.5	2.75	_	3.5	_		
	$V_{IH}$	10	$V_{out}=1.0$ or $9.0V$	7.0	_	7.0	5.50	-	7.0	_	1.5 3.0 V 4.0 - - V - mA	
		15	$V_{out} = 1.5 \text{ or } 13.5 \text{V}$	11.0		11.0	8.25	_	11.0	_		
		5.0	$V_{OH}=2.5V$	-1.0	_	-0.8	-1.7	-	-0.6	_		
	Іон	5.0	$V_{OH} = 4.6 \text{V}$	-0.20.16 -0.36 -		-0.12	_	1				
	10#	10	$V_{OH}=9.5\mathrm{V}$	-0.5		-0.4	-0.9	-	-0.3	_	V mA	
Output Drive Current		15	$V_{OH}=13.5\mathrm{V}$	-1.4		-1.2	-3.5	_	-1.0	-		
		5.0	$V_{OL}=0.4V$	0.52	_	0.44	0.88		0.36	-		
	IoL	10	$V_{OL}=0.5V$	1.3	_	1.1	2.25	_	0.9	2		
	j 1	15	$V_{OL} = 1.5 \text{V}$	3.6	_	3.0	8.8		2.4	_		
Input Current	I <sub>in</sub>	15		_	±0.3	_	±0.00001	±0.3	_	±1.0	μA	
Input Capacitance	$C_{in}$		$V_{in}=0$	-		_	5.0	7.5	_	-	рF	
	IDD 10 Zero Signal, - 40 - 0.010 40 -	_	150									
Quiescent Current		10		_	40	_	0.010	40	_	300	mA μA pF	μA
		15	per rackage	_	80	-	0.015	80	_	600		
	Ιτ	5.0	Dynamic+100,		_	-	0.6	_	- !		μΑ	
Total Supply Current*		10	$C_L = 50 \mathrm{pF}, f = 1 \mathrm{kHz},$		_	_	1.2	_	_	_		
		15	per Gate	_	_	-	1.8	_	-			

 $<sup>\</sup>boldsymbol{*}$  To calculate total supply current at frequency other than  $1\mathrm{kHz}.$ 

## **ESWITCHING CHARACTERISTICS** ( $C_L = 50 \text{pF}, T_d = 25^{\circ}\text{C}$ )

Characteristic	Symbol	$V_{DD}(\mathbf{V})$	min	typ	max	Unit
-		5.0	_	180	400	ns
Output Rise Time	t+	10	-	90	200	
		15	_	65	160	
		5.0	_	100	200	ns
Output Fall Time	t <sub>f</sub>	10	_	50	100	
		15		37	80	
		5.0		430	1125	ns
Propagation Delay Time	tphi.	10	-	180	450	
		15		130	330	

 $<sup>@</sup>V_{DD} = 5.0V \\ I_{T} = (0.6\mu\text{A/kHz})f + I_{DD} \\ @V_{DD} = 10V \\ I_{T} = (1.2\mu\text{A/kHz})f + I_{DD} \\ @V_{DD} = 15V \\ I_{T} = (1.8\mu\text{A/kHz})f + I_{DD} \\ @V_{DD} = 15V \\ I_{T} = (1.8\mu\text{A/kHz})f + I_{DD} \\ @V_{DD} = 15V \\ I_{T} = (1.8\mu\text{A/kHz})f + I_{DD} \\ @V_{DD} = 10V \\ I_{T} = (1.8\mu\text{A/kHz})f + I_{DD} \\ @V_{DD} = 15V \\ I_{T} = (1.8\mu\text{A/kHz})f + I_{DD} \\ @V_{DD} = 10V \\ &V_{DD} = 10V \\ &V_{D$ 

Unit: mm 19.20 20.00 Max 16 7.40 Max 6.30 1.3 1.11 Max 7.62 5.06 Max 2.54 Min 0.51 Min  $0.25^{+0.13}_{-0.05}$  $0.48 \pm 0.10$  $2.54\pm0.25$  $0^{\circ} - 15^{\circ}$ Hitachi Code DP-16 **JEDEC** Conforms EIAJ Conforms Weight (reference value) 1.07 g

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