

UMIL 100

100 Watts, 28 Volts, Class AB Defcom 225 - 400 MHz

GENERAL DESCRIPTIO The UMIL100 is a double input matched CO transistor specifically intended for use in the It may be operated in Class AB or C. Gold resistors ensure ruggedness and high reliability	CASE OUTLINE 55HV, Style 2	
ABSOLUTE MAXIMUM Maximum Power Dissipation @ 25°C	RATINGS 250 Watts	
Maximum Voltage and Current		
BVces Collector to Emiter Voltage	60 Volts	
BVebo Emitter to Base Voltage	4.0 Volts	
Ic Collector Current	12.0 A	
Maximum Temperatures		
Storage Temperature	- 65 to +150°C	
Operating Junction Temperature	+150°C	

ELECTRICAL CHARACTERISTICS @ 25 °C

SYMBOL	CHARACTERISTICS	TEST CONDITIONS	MIN	ТҮР	MAX	UNITS
Pout Pin Pg ηc VSWR	Power Output Power Input Power Gain Efficiency Load Mismatch Tolerance	F = 400 MHz Vcc = 28 Volts	100 7.2	8.5 55	19 5:1	Watts Watts dB %

BVebo	Emitter to Base Breakdown	Ie = 5 mA	4.0			Volts
BVces BVceo	Collector to Emitter Breakdown Collector to Emitter Breakdown	Ic = 100 mA $Ie = 50 mA$	60 31			Volts Volts
BVcbo	Collector to Base Breakdown	Ic = 30 mA Ic = 100 mA	60			Volts
Icbo	Collector to Base Current	Vc = 30 Volts			50	mA
Cob	Output Capacitance	Vcb = 28 V, F = 1 MHz		120		pF
h _{FE}	DC - Current Gain	Vce = 5 V, Ic = 1 A	10			
θjc	Thermal Resistance				0.7	°C/W

Issue October 1998: Correction on Case from HU to HV

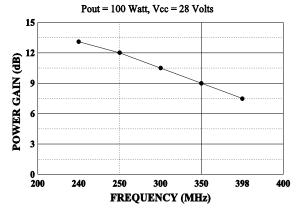
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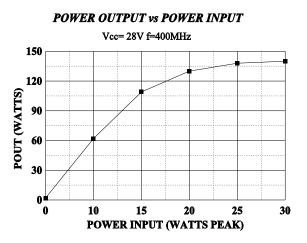
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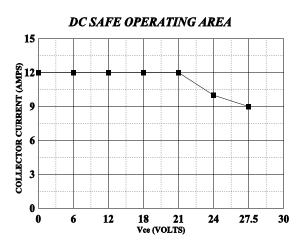


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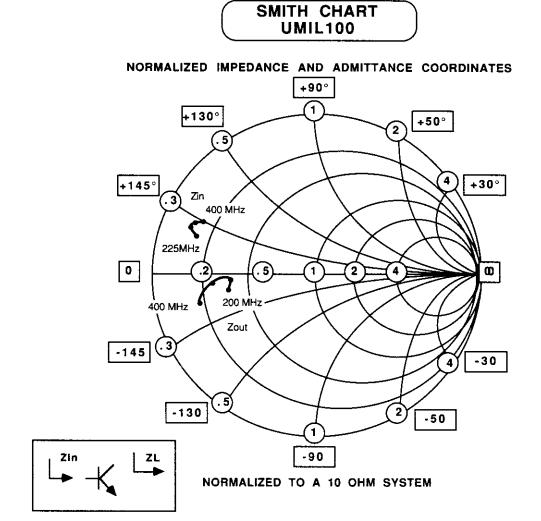
POWER GAIN VS FREQUENCY







August 1996



FREQUENCY MHz	R	in JX	FREQUENCY MHz	Zio R	ad JX
225	1.7	+ j2.2	225	3.5	- j1.5
300	1.2	+ j2.4	300	3.8	- j0.9
350	1.3	+ j2.6	350	2.7	- j1.0
400	1.5	+ j2.8	400	1.8	- j2.0