

DK13..FW

DS4267-4.0 July 2001

Fast Switching Thyristor

Replaces January 2000 version, DS4267-3.0

FEATURES

- Low Switching Losses At High Frequency.
- Fully Characterised For Operation Up To 20kHz.

APPLICATIONS

- High Power Inverters And Choppers.
- UPS.
- AC Motor Drives.
- Induction Heating.
- Cycloconverters.

VOLTAGE RATINGS

Type Number	Repetitive Peak Voltages V _{DRM} V _{RRM} V	Conditions
DK13 08FW K or M DK13 06FW K or M		$V_{RSM} = V_{RRM} + 100V$ $I_{DRM} = I_{RRM} = 15mA$ at V _{RRM} or V _{DRM} & T _{vj}

ORDERING INFORMATION

When ordering, select the required part number shown in the Voltage Ratings selection table, then:-

Add K to type number for 1/2" 20 UNF thread, e.g. **DK13 06FWK** or

Add M to type number for M12 thread, e.g. DK13 06FM.

Note: Please use the complete part number when ordering and quote this number in any future correspondance relating to your order.

KEY PARAMETERS				
\mathbf{V}_{drm}	800V			
I _{T(RMS)}	110A			
I _{tsm}	1200A			
dVdt	200V/ μs			
dl/dt	200A/ μs			
t _q	10 μs			

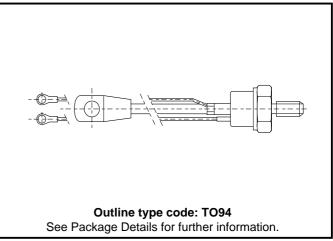


Fig. 1 Package outline



CURRENT RATINGS

Symbol	Parameter	Conditions	Max.	Units
I _{T(AV)}	Mean on-state current	Half wave resistive load, $T_{case} = 80^{\circ}C$	70	А
I _{T(RMS)}	RMS value	$T_{case} = 80^{\circ}C$	110	А

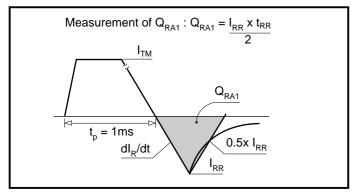
SURGE RATINGS

Symbol	Parameter	Conditions	Max.	Units
I _{TSM}	Surge (non-repetitive) on-state current	$t_p = 10ms$ half sine; $T_{case} = 125^{\circ}C$	1.2	kA
l ² t	I ² t for fusing	V _R = 0% V _{RRM} - 1/4 sine	7.2 x 10 ³	A²s

THERMAL AND MECHANICAL DATA

Symbol	Parameter	Conditions	Min.	Max.	Units
R _{th(j-c)}	Thermal resistance - junction to case	dc	-	0.24	°C/W
R _{th(c-h)}	Thermal resistance - case to heatsink	Mounting torque 15.0Nm with mounting compound	-	0.08	°C/W
T _{vj}	Virtual junction temperature	On-state (conducting)	-	125	°C
		Reverse (blocking)	-	125	°C
T _{stg}	Storage temperature range		-40	150	°C
-	Mounting torque		12.0	15.0	Nm

MEASUREMENT OF RECOVERED CHARGE - \mathbf{Q}_{RA1}





DYNAMIC CHARACTERISTICS

Symbol	Parameter	Conditions		Min.	Max.	Units
V _{TM}	Maximum on-state voltage	At 300A peak, T _{case} = 25°C	At 300A peak, T _{case} = 25°C		2.35	V
I _{RRM} /I _{DRM}	Peak reverse and off-state current	At $V_{\text{RRM}}/V_{\text{DRM}}$, $T_{\text{case}} = 125^{\circ}\text{C}$		-	15	mA
dV/dt	Maximum linear rate of rise of off-state voltage	Linear to 60% $V_{DRM} T_j = 125^{\circ}C$,	Gate open circuit	-	200	V/µs
-11/-14		Gate source 20V, 20Ω	Repetitive 50Hz	-	500	A/μs
dl/dt	Rate of rise of on-state current	t _r < 0.5μs, T _j = 125°C	Non-repetitive	-	800	A/μs
V _{T(TO)}	Threshold voltage	At T _{vj} = 125°C		-	1.65	V
r _⊤	On-state slope resistance	At T _{vj} = 125°C		-	3.5	mΩ
t _{gd}	Delay time	$T_j = 25^{\circ}C, I_T = 50A,$ $V_D = 300V, I_G = 1A,$ $dI/dt = 50A/\mu s, dI_G/dt = 1A/\mu s$		-	3	μs
t _{(ON)TOT}	Total turn-on time			-	1.5	μs
I _H	Holding current	$T_{j} = 25^{\circ}C, I_{TM} = 1A, V_{D} = 12V$		60*	-	mA
t _q	Turn-off time	$\begin{array}{l} T_{j}=125^{\circ}C,\ I_{T}=100A,\ V_{R}=50V,\\ dV/dt=200V/\mu s\ (Linear\ to\ 60\%\ V_{DRM}),\\ dI_{R}/dt=30A/\mu s,\ Gate\ open\ circuit \end{array}, \begin{array}{c} t_{q}\ code:\ W$		-	10	μs

*Typical value.

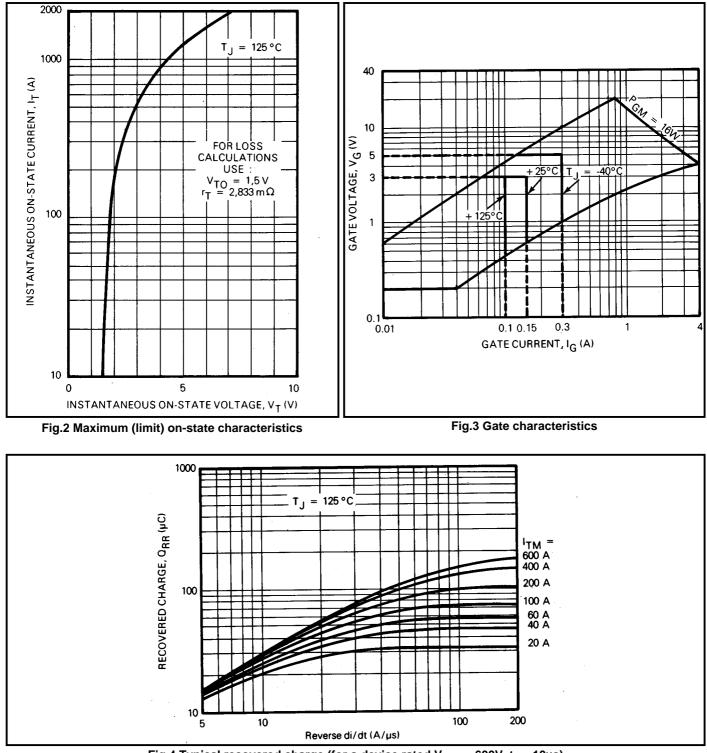
GATE TRIGGER CHARACTERISTICS AND RATINGS

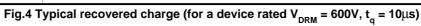
Symbol	Parameter	Conditions		Max.	Units
V _{GT}	Gate trigger voltage	$V_{\text{DRM}} = 12V, T_{\text{case}} = 25^{\circ}\text{C}, R_{\text{L}} = 6\Omega$	-	3.0	V
I _{GT}	Gate trigger current	$V_{\text{DRM}} = 12V, \ T_{\text{case}} = 25^{\circ}\text{C}, \ R_{\text{L}} = 6\Omega$	-	200	mA
V _{gd}	Gate non-trigger voltage	At $V_{\text{DRM}} T_{\text{case}} = 125^{\circ}\text{C}$, $R_{\text{L}} = 1\text{k}\Omega$	-	0.2	V
V _{rgm}	Peak reverse gate voltage		-	5.0	V
I _{FGM}	Peak forward gate current	Anode positive with respect to cathode	-	4	Α
P _{GM}	Peak gate power		-	16	w
P _{G(AV)}	Mean gate power		-	3.0	W





CURVES







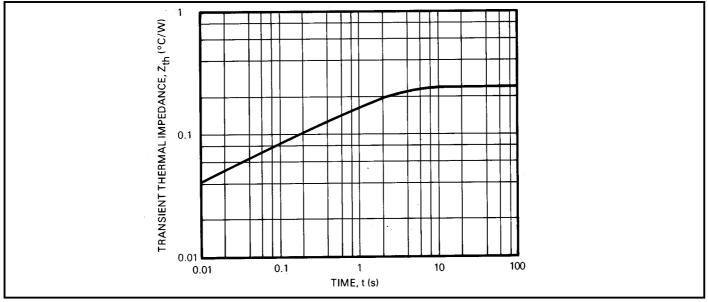


Fig.5 Transient thermal impedance - junction to case

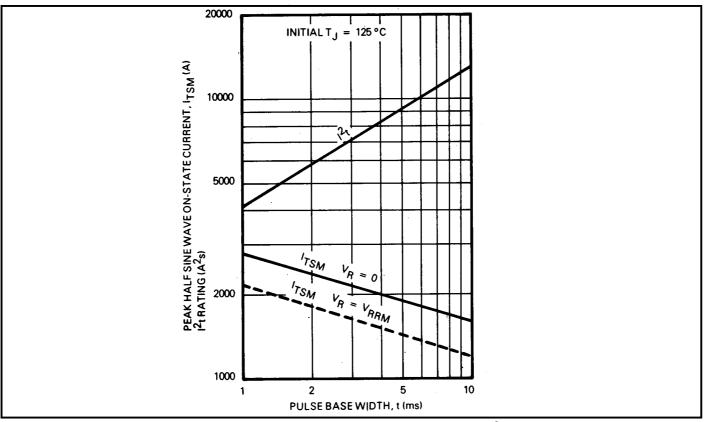
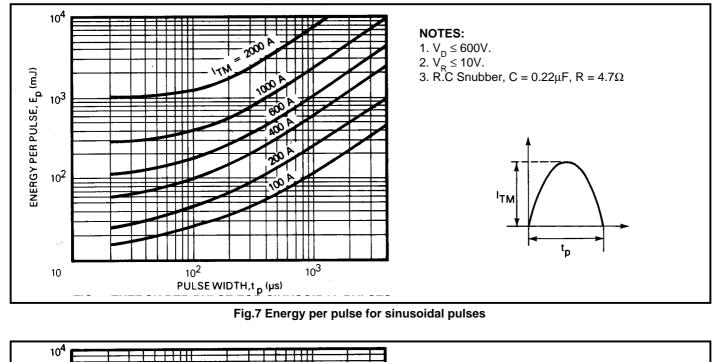


Fig.6 Non-repetitive sub-cycle surge on-state current and I²t rating





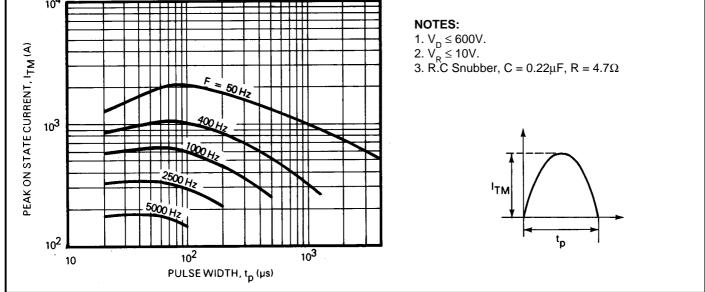


Fig.8 Maximum allowable peak on-state current vs pulse width for $T_{case} = 65^{\circ}C$



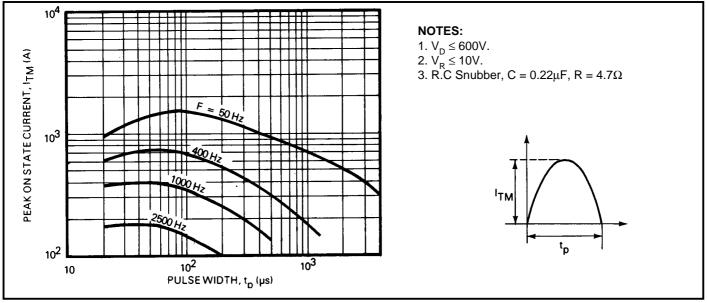


Fig.9 Maximum allowable peak on-state current vs pulse width for $T_{case} = 90^{\circ}C$

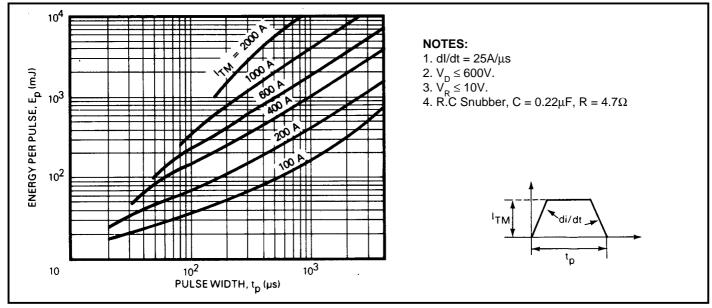


Fig.10 Energy per pulse for trapezoidal pulses



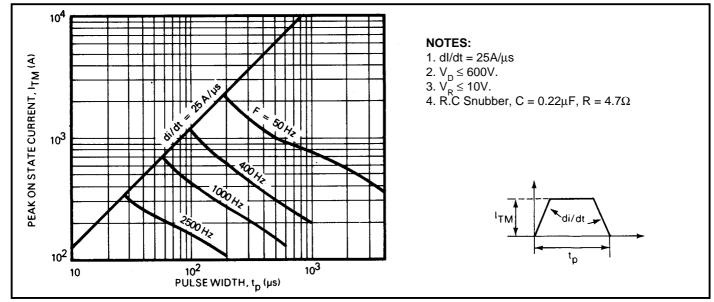


Fig.11 Maximum allowable peak on-state current vs pulse width for $T_{case} = 65^{\circ}C$

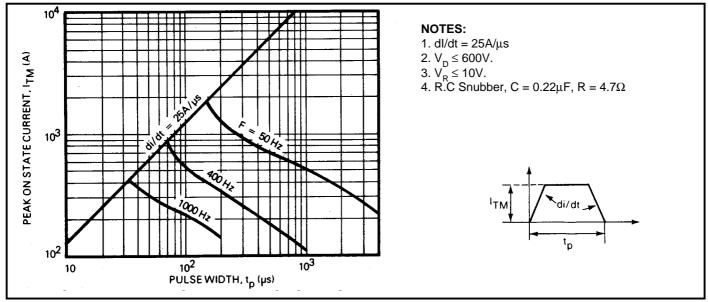


Fig.12 Maximum allowable peak on-state current vs pulse width for T_{case} = 90°C



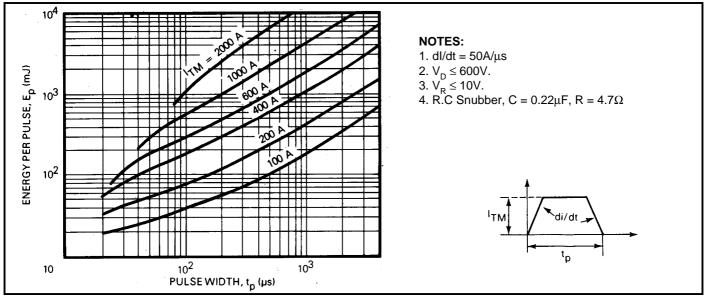


Fig.13 Energy per pulse for trapezoidal pulses

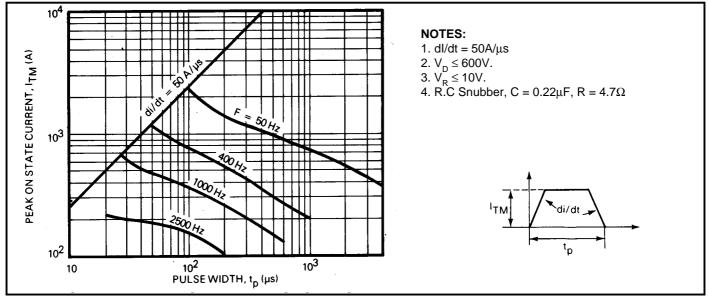


Fig.14 Maximum allowable peak on-state current vs pulse width for $T_{case} = 65^{\circ}C$



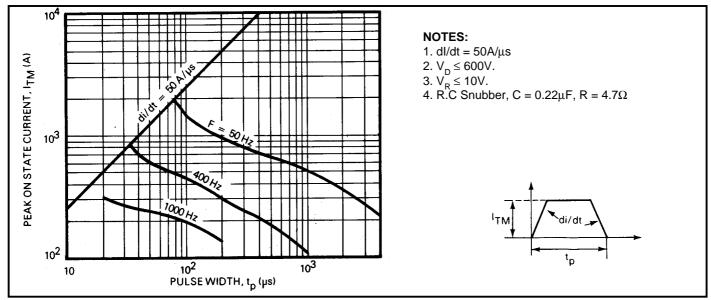


Fig.15 Maximum allowable peak on-state current vs pulse width for $T_{case} = 65^{\circ}C$

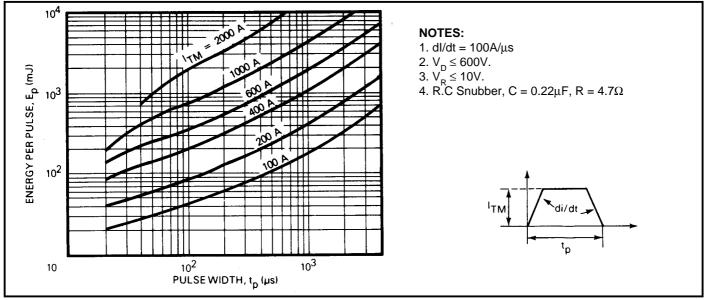


Fig.16 Energy per pulse for trapezoidal pulses



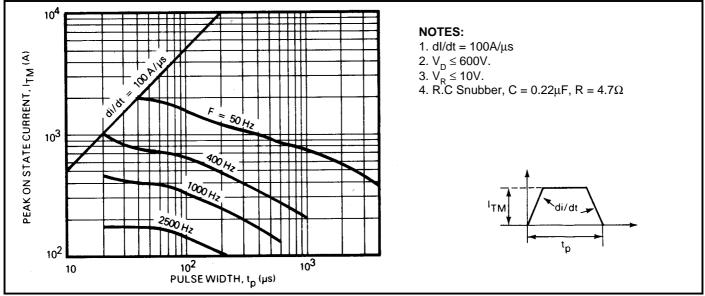


Fig.17 Maximum allowable peak on-state current vs pulse width for $T_{case} = 65^{\circ}C$

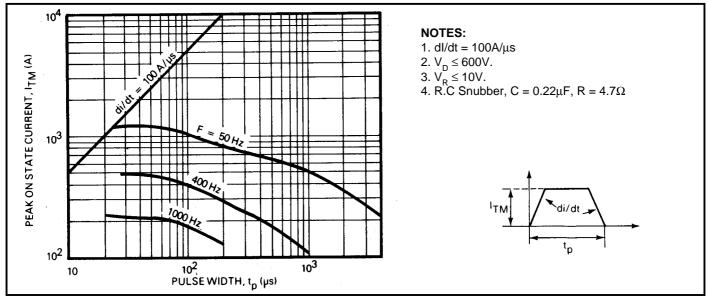
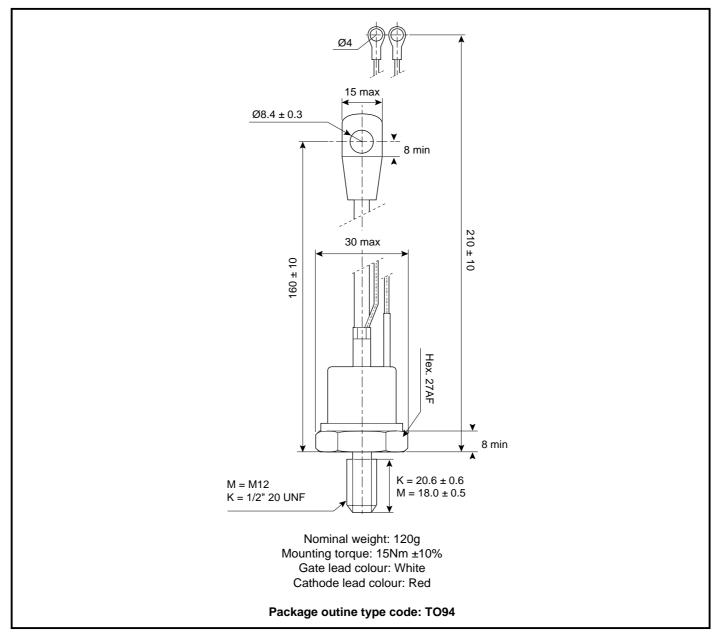


Fig.18 Maximum allowable peak on-state current vs pulse width for $T_{case} = 90^{\circ}C$



PACKAGE DETAILS

For further package information, please contact your nearest Customer Service Centre. All dimensions in mm, unless stated otherwise. DO NOT SCALE.





POWER ASSEMBLY CAPABILITY

The Power Assembly group was set up to provide a support service for those customers requiring more than the basic semiconductor, and has developed a flexible range of heatsink and clamping systems in line with advances in device voltages and current capability of our semiconductors.

We offer an extensive range of air and liquid cooled assemblies covering the full range of circuit designs in general use today. The Assembly group continues to offer high quality engineering support dedicated to designing new units to satisfy the growing needs of our customers.

Using the latest CAD methods our team of design and applications engineers aim to provide the Power Assembly Complete Solution (PACs).

DEVICE CLAMPS

Disc devices require the correct clamping force to ensure their safe operation. The PACS range includes a varied selection of pre-loaded clamps to suit all of our manufactured devices. Types available include cube clamps for single side cooling of 'T' 23mm and 'E' 30mm discs, and bar clamps right up to 83kN for our 'Z' 100mm thyristors and diodes.

Clamps are available for single or double side cooling, with high insulation versions for high voltage assemblies.

Please refer to our application note on device clamping, AN4839

HEATSINKS

The Power Assembly group has its own proprietary range of extruded aluminium heatsinks. They have been designed to optimise the performance of Dynex semiconductors. Data with respect to air natural, forced air and liquid cooling (with flow rates) is available on request.

For further information on device clamps, heatsinks and assemblies, please contact your nearest sales representative or customer service office.



Datasheet Annotations:

Dynex Semiconductor annotate datasheets in the top right hard corner of the front page, to indicate product status. The annotations are as follows:-

Target Information: This is the most tentative form of information and represents a very preliminary specification. No actual design work on the product has been started.

Preliminary Information: The product is in design and development. The datasheet represents the product as it is understood but details may change.

Advance Information: The product design is complete and final characterisation for volume production is well in hand.

No Annotation: The product parameters are fixed and the product is available to datasheet specification.

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