# FIXED HIGH B.W. DELAY LINE T<sub>R</sub> < 1ns (SERIES 2075 & 2075A)



2075-xx

2075A

 $xx = Delay(T_D)$ 

## FEATURES

- Microstrip Technology
- Fast rise time for high frequency applications
- Fixed delays available from 300ps to 6ns
- Mechanically variable delay available (2075A) •
- I/O reversible •
- **BNC** female connectors
- Meets or exceeds MIL-D-23859C

#### FUNCTIONAL DESCRIPTION

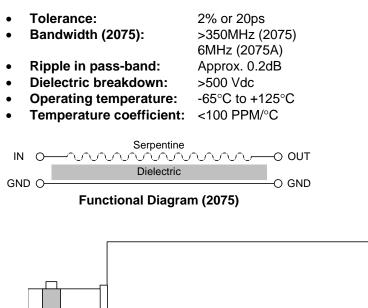
The 2075- and 2075A-series devices are single-input, single-output, passive delay lines. For the 2075, the signal input (IN) is reproduced at the output (OUT), shifted by a time  $(T_D)$  given by the device dash number. The rise time  $(T_R)$  of the lines is no more than 1ns, resulting in a 3dB

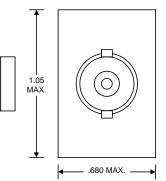
**PIN DESCRIPTIONS** 

IN Signal Input (BNC) OUT Signal Output (BNC)

bandwidth of at least 350MHz. For the 2075A, the delay is mechanically variable from 3ns to 7ns and the bandwidth is 6MHz. The characteristic impedance of both lines is nominally 75 ohms.

### SERIES SPECIFICATIONS





©2001 Data Delay Devices

Package Dimensions

2.00 MAX.

Adjustment screw (2075A only)

IN

IN

**DASH NUMBER** 

Part	Delay	Imped.
Number	(ps)	(Ω)
2075-300	300 ± 20	75
2075-500	$500 \pm 20$	75
2075-1000	$1000 \pm 20$	75
2075-2000	$2000 \pm 40$	75
2075-3000	$3000 \pm 60$	75
2075-4000	$4000 \pm 80$	75
2075-5000	$5000 \pm 100$	75
2075-6000	$6000 \pm 120$	75
2075A	3-7ns	75

SPECIFICATIONS

## PACKAGES

ηουτ

₽оот

# **PASSIVE DELAY LINE TEST SPECIFICATIONS**

#### **TEST CONDITIONS**

INPUT:	
Ambient Temperature:	$25^{\circ}C \pm 3^{\circ}C$
Source Amplitude:	0dBm typical
Source Impedance:	75 $\Omega$ nominal
Input Frequency:	27.77778MHz

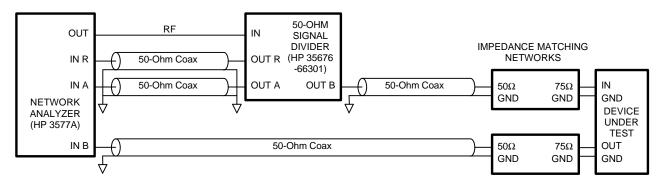
OUTPUT:

Z<sub>load</sub>:

 $75\Omega$  nominal

Network analyzer is used in phase measurement mode, normalized with a calibrated BNC jumper between input and output signals. Delay is related to phase lag with proportionality constant of 100ps/deg.

**NOTE:** The above conditions are for test only and do not in any way restrict the operation of the device.



**Test Setup**