

CMLB6263
PICOmini™
HIGH VOLTAGE
SCHOTTKY BRIDGE RECTIFIER

PICOmini™



SOT-563 CASE

Central™

Semiconductor Corp.

FEATURES:

- HIGH VOLTAGE (70V)
- LOW FORWARD VOLTAGE

MARKING CODE: 63B

DESCRIPTION:

The Central Semiconductor CMLB6263 incorporates four, high voltage, low V_F Schottky diodes arranged in a full wave bridge rectifier configuration, in a space saving SOT-563 surface mount package. This device is designed for fast switching applications requiring a low forward voltage drop.

MAXIMUM RATINGS: ($T_A=25^\circ\text{C}$)

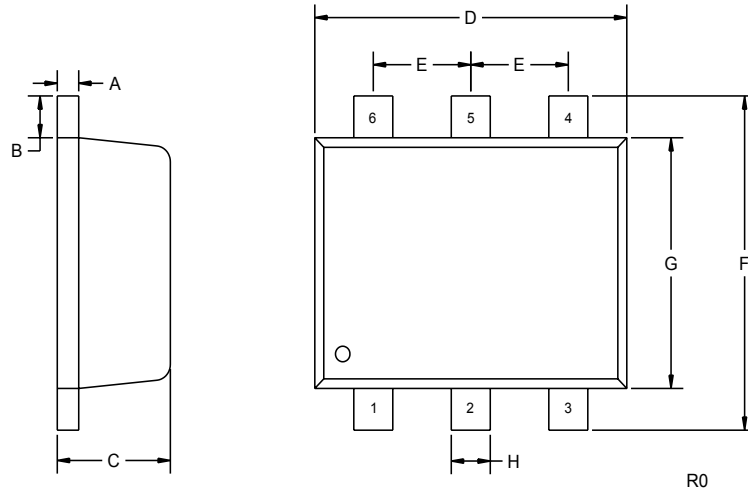
	<u>SYMBOL</u>		<u>UNITS</u>
Peak Repetitive Reverse Voltage	V_{RRM}	70	V
Continuous Forward Current	I_F	15	mA
Forward Surge Current, $t_p=1.0$ s	I_{FSM}	50	mA
Power Dissipation	P_D	250	mW
Operating and Storage			
Junction Temperature	T_J, T_{stg}	-65 to +150	$^\circ\text{C}$
Thermal Resistance	Θ_{JA}	500	$^\circ\text{C/W}$

ELECTRICAL CHARACTERISTICS PER DIODE: ($T_A=25^\circ\text{C}$)

<u>SYMBOL</u>	<u>TEST CONDITIONS</u>	<u>MIN</u>	<u>TYP</u>	<u>MAX</u>	<u>UNITS</u>
I_R	$V_R=50\text{V}$		98	200	nA
BV_R	$I_R=10\mu\text{A}$	70			V
V_F	$I_F=1.0\text{mA}$		395	410	mV
C_T	$V_R=0\text{V}, f=1.0\text{MHz}$			2.0	pF
t_{rr}	$I_R=I_F=10\text{mA}, I_{rr}=1.0\text{mA}, R_L=100\Omega$			5.0	ns

R0 (13-December 2002)

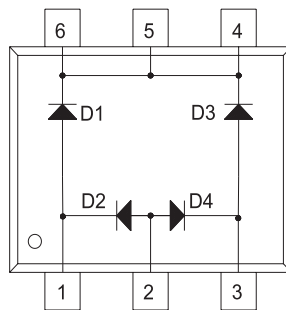
SOT-563 CASE - MECHANICAL OUTLINE



SYMBOL	DIMENSIONS			
	INCHES		MILLIMETERS	
	MIN	MAX	MIN	MAX
A	0.004	0.007	0.10	0.18
B	0.008		0.20	
C	0.022	0.024	0.56	0.60
D	0.059	0.067	1.50	1.70
E	0.020		0.50	
F	0.061	0.067	1.55	1.70
G	0.047		1.20	
H	0.006	0.012	0.15	0.30

SOT-563 (REV: R0)

Bridge Configuration



LEAD CODE:

- 1) AC
- 2) -
- 3) AC
- 4) +
- 5) +
- 6) +

MARKING CODE: 63B

R0 (13-December 2002)