

Praetorian[™] L-C EMI Filter with ESD Protection for Headset Speaker Applications

Features

- · 2 channels of EMI filtering
- ±30kV ESD protection
- (IEC 61000-4-2, contact discharge)
- ±30kV ESD protection (HBM)
- OptiGuard[™] Coating for improved reliability at assembly
- · Greater than 35dB of attenuation at 1GHz
- 6-bump, 1.720mm x 1.220mm footprint Chip Scale Package (CSP)
- Lead-free version available

Applications

- Headset Speaker port in mobile handsets
- I/O port protection for mobile handsets, notebook computers, PDAs etc.
- EMI filtering for data ports in cell phones, PDAs or notebook computers.

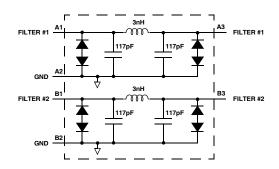
Product Description

California Micro Devices' CM1418 is an L-C EMI filter array with ESD protection, which integrates two Pifilters (C-L-C) for the headset speaker. The CM1418 has component values of 117pF-3.0nH-117pF. The parts include ESD protection diodes on all input/output pins, which provide a very high level of protection for sensitive electronic components that may be subjected to electrostatic discharge (ESD). The ESD diodes connected to the filter ports safely dissipate ESD strikes of ± 30 kV, beyond the maximum requirement of the IEC61000-4-2 international standard. Using the MIL-STD-883 (Method 3015) specification for Human Body Model (HBM) ESD, the pins are protected for contact discharges at greater than ± 30 kV.

This device is particularly well suited for portable electronics (e.g. mobile handsets, PDAs, notebook computers) because of its small package format and easy-to-use pin assignments. In particular, the CM1418 is ideal for EMI filtering and protecting speaker output lines from ESD for the headset speaker in mobile handsets. Most speakers have impedance of 8Ω and in order to maximize the power output, the resistance of an EMI filter needs to be as low as possible and the CM1418 addresses this by having a C-L-C based EMI filter where the inductor has less than 0.35Ω of resistance.

The CM1418 is available either uncoated or with *Opti-Guard*™ coating resulting in improved reliability at assembly. The CM1418 is also available in a space saving, low profile Chip Scale Package with optional lead-free finishing.

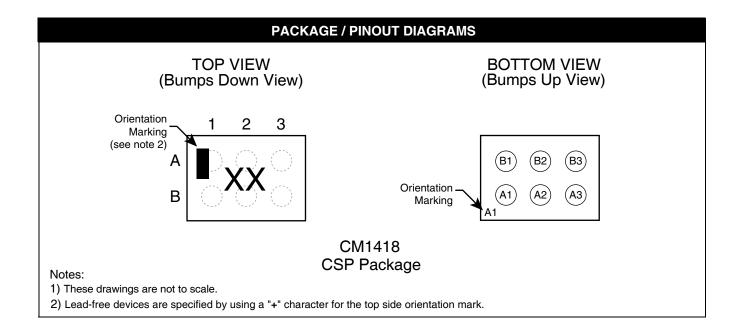
Electrical Schematic



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PIN DESCRIPTIONS						
PIN	NAME	DESCRIPTION				
A1	Filter #1	Filter #1 Input				
A2	GND	Device Ground				
А3	Filter #1	Filter #1 Input				
B1	Filter #2	Filter #2 Input				
B2	GND	Device Ground				
В3	Filter #2	Filter #2 Input				

Ordering Information

PART NUMBERING INFORMATION									
			Standard	Finish	Lead-free Finish ²				
Pins	Package	<i>OptiGuard</i> [™] Coating	Ordering Part Number ¹ Part Marking		Ordering Part Number ¹	Part Marking			
6	CSP	Y	CM1418-02CS	CG	CM1418-02CP	CG			
6	CSP	N	CM1418-0BCS	AL	CM1418-0BCP	AL			

Note 1: Parts are shipped in Tape & Reel form unless otherwise specified.

Note 2: Lead-free devices are specified by using a "+" character for the top side orientation mark.



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Specifications

ABSOLUTE MAXIMUM RATINGS						
PARAMETER	RATING	UNITS				
Storage Temperature Range	-65 to +150	°C				
DC Current per Inductor	30	mA				
DC Package Power Rating	0.5	W				

STANDARD OPERATING CONDITIONS							
PARAMETER	RATING	UNITS					
Operating Temperature Range	-40 to +85	°C					

	ELECTRICAL OPERATING CHARACTERISTICS (SEE NOTE 1)								
SYMBOL	PARAMETER	CONDITIONS	MIN	TYP	MAX	UNITS			
L	Inductance			3.0		nΗ			
R	DC Channel Resistance			0.28	0.35	Ω			
C _{TOT}	Total Channel Capacitance	2.5V dc; 1MHz, 30mV ac	187	234	281	pF			
C ₁	Capacitance C ₁	2.5V dc; 1MHz, 30mV ac	93	117	140	pF			
V _{ST}	Stand-off Voltage	Ι = 10μΑ		6.0		V			
I _{LEAK}	Diode Leakage Current	$V_{IN} = \pm 3.3V$		0.1	1.0	μΑ			
V _{SIG}	Signal Clamp Voltage Positive Clamp Negative Clamp	I _{LOAD} = 10mA I _{LOAD} = -10mA	5.6 -9.0	6.8 -6.8	9.0 -5.6	V V			
V _{ESD}	In-system ESD Withstand Voltage a) Human Body Model, MIL-STD-883, Method 3015 b) Contact Discharge per IEC 61000-4-2 Level 4	Notes 2 and 3	±30 ±30			kV kV			
R _{DYN}	Dynamic Resistance Positive Negative			0.95 0.90		Ω			
f _C	Cut-off frequency $Z_{SOURCE} = 50\Omega$, $Z_{LOAD} = 50\Omega$	L = 3nH, C = 117pF		22		MHz			

Note 1: $T_A=25$ °C unless otherwise specified.

Note 2: ESD applied to input and output pins with respect to GND, one at a time. Note 3: These parameters are guaranteed by design and characterization.

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Performance Information

Typical Filter Performance (nominal conditions unless specified otherwise, 50 Ohm Environment)

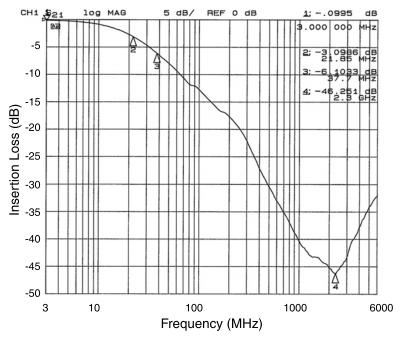


Figure 1. Insertion Loss vs. Frequency (Filter #1 to GND B2)

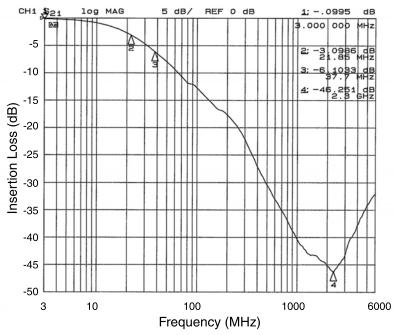


Figure 2. Insertion Loss vs. Frequency (Filter #2 to GND B2)



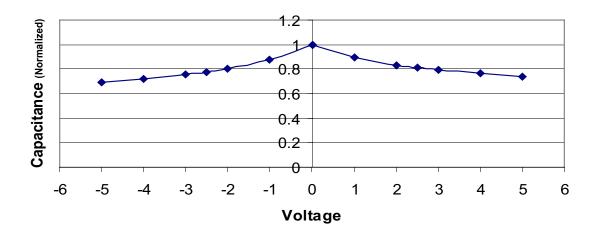


Figure 3. Typical Diode Capacitance vs. Input Voltage (normalized to 2.5VDC)

12/13/05

CM1418



Application Information

Refer to Application Note AP-217, "The Chip Scale Package", for a detailed description of Chip Scale Packages offered by California Micro Devices.

PRINTED CIRCUIT BOARD RECOMMENDATIONS						
PARAMETER	VALUE					
Pad Size on PCB	0.275mm					
Pad Shape	Round					
Pad Definition	Non-Solder Mask defined pads					
Solder Mask Opening	0.325mm Round					
Solder Stencil Thickness	0.125 - 0.150mm					
Solder Stencil Aperture Opening (laser cut, 5% tapered walls)	0.330mm Round					
Solder Flux Ratio	50/50 by volume					
Solder Paste Type	No Clean					
Pad Protective Finish	OSP (Entek Cu Plus 106A)					
Tolerance — Edge To Corner Ball	<u>+</u> 50μm					
Solder Ball Side Coplanarity	<u>+</u> 20μm					
Maximum Dwell Time Above Liquidous	60 seconds					
Maximum Soldering Temperature for Eutectic Devices using a Eutectic Solder Paste	240°C					
Maximum Soldering Temperature for Lead-free Devices using a Lead-free Solder Paste	260°C					

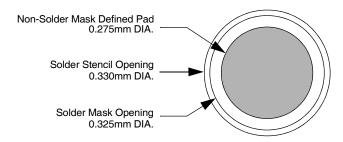


Figure 4. Recommended Non-Solder Mask Defined Pad Illustration

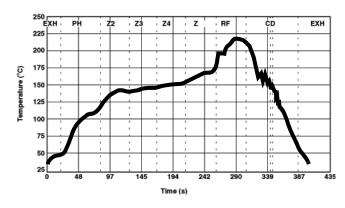


Figure 5. Eutectic (SnPb) Solder **Ball Reflow Profile**

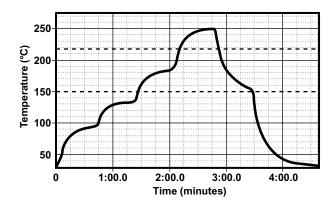


Figure 6. Lead-free (SnAgCu) Solder **Ball Reflow Profile**

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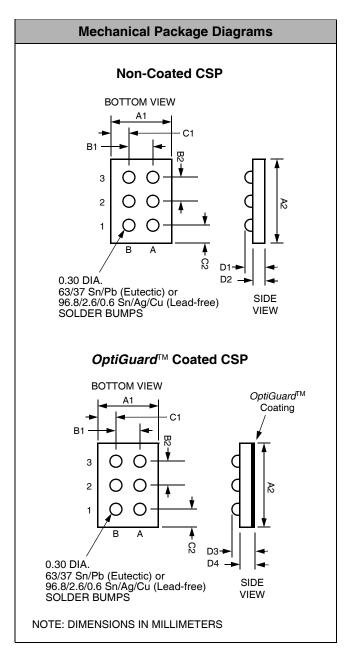


Mechanical Details

CM1418 CSP Mechanical Specifications

The CM1418 is supplied in 6-bump Chip Scale Package (CSP). Dimensions are presented below.

PACKAGE DIMENSIONS								
Pack	age	Custom CSP						
Bumps		6						
Dim	M	lillimete	rs	Inches				
Dilli	Min	Nom	Max	Min	Nom	Max		
A1	1.175	1.220	1.265	0.0463	0.0480	0.0498		
A2	1.675	1.720	1.765	0.0659	0.0677	0.0695		
B1	0.495	0.500	0.505	0.0195 0.0197		0.0199		
B2	0.495	0.500	0.505	0.0195	0.0197	0.0199		
C1	0.310	0.360	0.410	0.0122	0.0142	0.0161		
C2	0.310	0.360	0.410	0.0122	0.0142	0.0161		
D1	0.562	0.606	0.650	0.0221	0.0239	0.0256		
D2	0.356	0.381	0.406	0.0140	0.0150	0.0160		
D3	0.575	0.644	0.714	0.0226	0.0254	0.0281		
D4	0.368	0.419 0.470		0.0145 0.0165		0.0185		
# per tape and reel		3500 pieces						
Controlling dimension: millimeters								



Package Dimensions for CM1418-0xCS/CP 6-bump Chip Scale Package

7



Mechanical Details (cont'd)

CSP Tape and Reel Specifications

PART NUMBER	CHIP SIZE (mm)	POCKET SIZE (mm) B ₀ X A ₀ X K ₀	TAPE WIDTH W	REEL DIA.	QTY PER REEL	P ₀	P ₁
CM1418-02CS/CP	1.72 X 1.22 X 0.67	2.08 x 1.45 x 0.71	8mm	178mm (7")	3500	4mm	4mm
CM1418-02CS/CP	1.72 X 1.22 X 0.61	2.08 x 1.45 x 0.71	8mm	178mm (7")	3500	4mm	4mm

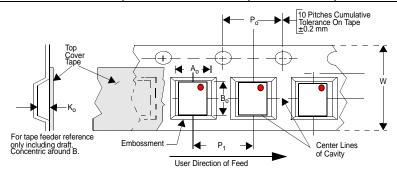


Figure 7. Tape and Reel Mechanical DataTape and Reel Mechanical Data